

~~16/08/2019~~

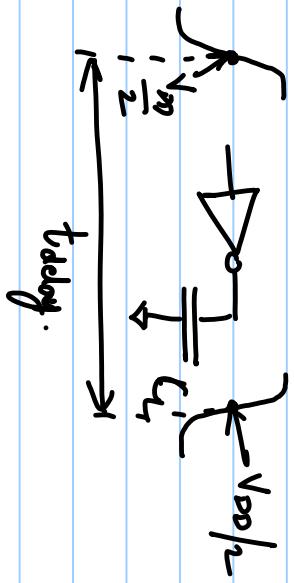
EE5311

Module 1: The Transistor

PMOS Transistor  $\rightarrow \{K_p, \lambda_p, V_{Amp}, V_{rop}, Y_p\}$  all are -ve numbers.

$$I_D = K_p \frac{V}{L} V_{max} [ (V_{GS} - V_T) - \frac{V_{max}}{2} ] (1 + \lambda V_{DS})$$

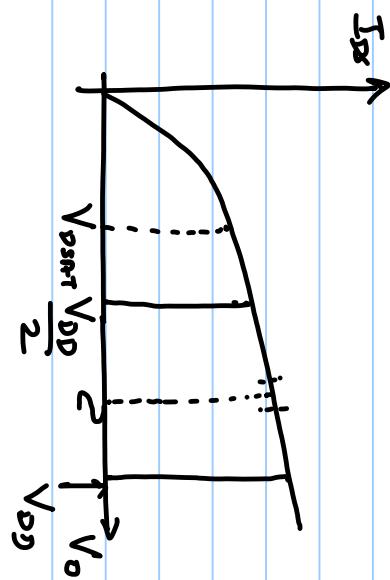
## CAPACITANCE CHARGING / DISCHARGING



$t_{delay}$

$$V_o \rightarrow (V_{DD} \text{ to } V_{DD}/2)$$

$$\frac{1}{R + R'} C_L (V_o(\delta) = V_{DD})$$



instantaneous

$$I_{DS} = k_n \frac{W}{L} V_{DD} \lambda m \left[ (V_{DD} - V_{TN}) - \frac{V_{DD} \lambda m}{2} \right] (1 + \lambda V_o)$$

$$= I_0 (1 + \lambda V_o)$$

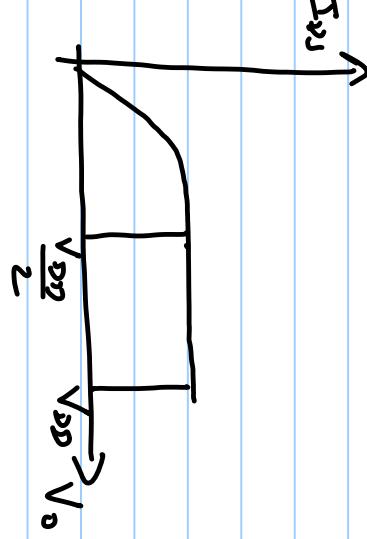
$$R(V_0) = \frac{V_0}{I_0(1+\lambda V_0)}$$

$$R_{\text{eq}} = \frac{1}{(V_f - V_i)} \int_{V_i}^{V_f} R(V_0) dV_0 \sim \frac{1}{(-V_{DD}/2)} \int_{V_{DD}}^{V_{DD}/2} \frac{V_0}{I_0} (1 - \lambda V_0) dV_0$$

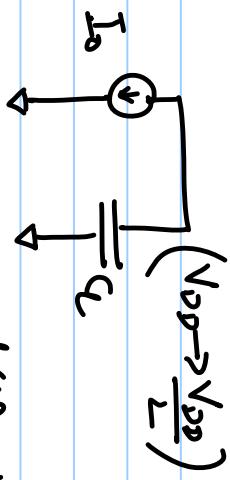
$$= \frac{1}{I_0} \cdot \frac{2}{V_{DD}} \int_{V_{DD}/2}^{V_{DD}} V_0 (1 - \lambda V_0) dV_0$$

$$= \frac{3}{4} \frac{V_{DD}}{I_0} - \frac{1}{12} \frac{\lambda V_{DD}^2}{I_0}$$

$$\Rightarrow R_{\text{eq}} = \frac{3}{4} \frac{V_{DD}}{I_0} \quad (\text{if } \lambda = 0)$$

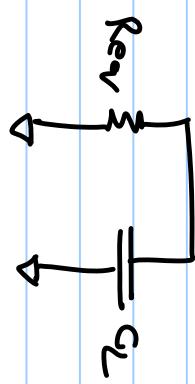


METHOD 2:



$$\tau = \frac{C_L V_{DD}}{I_o} = \frac{C_L (V_{DD}/2)}{I_o}$$

$$V_o(t) = V_{DD} - \frac{I_o}{C_L} t \leftarrow$$

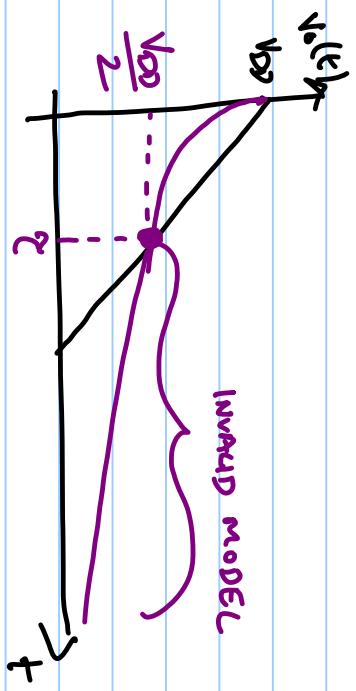


$$R_{eq} = \frac{3}{4} \frac{V_{DD}}{I_o}$$

$$\Rightarrow \tau' = R_{eq} C$$

$$\tau = 0.693 \cdot \frac{3}{4} \frac{V_{DD} C_L}{I_o} \approx 0.5 \frac{V_{DD} C_L}{I_o}$$

METHOD 1:



$$R_{eq} = \frac{3}{4} \frac{V_{DD}}{I_0}$$

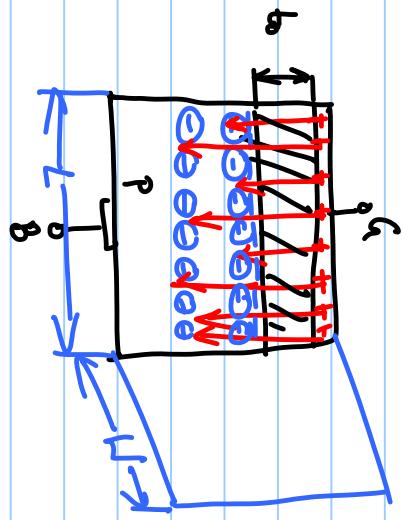
$$I_0 = \begin{cases} k_n \frac{U}{L} V_{DD} m \left[ (V_{DD} - V_{Tn}) - \sqrt{2e\sigma m} \right] & \hookrightarrow V_{EL} \text{ SAT (sc)} \\ \frac{1}{2} k_n \frac{U}{L} (V_{DD} - V_T)^2 - \text{SAT (long)} \end{cases}$$

$$R_{eq} \propto \frac{1}{W}$$

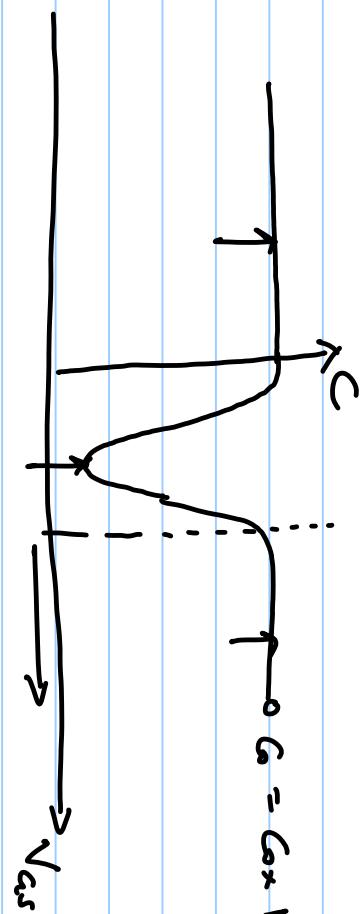
if  $V_{DD}$  large i.e  $V_{DD} - V_{Tn} - \frac{\sqrt{2e\sigma m}}{2} \sim V_{DD}$

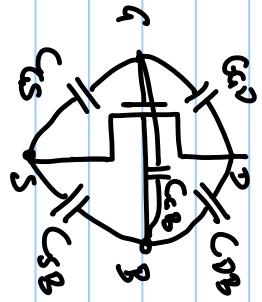
$R_{eq}$  not a fn of  $V_{DD}$

CAPACITANCE:



$$C = C_0 \times W L$$

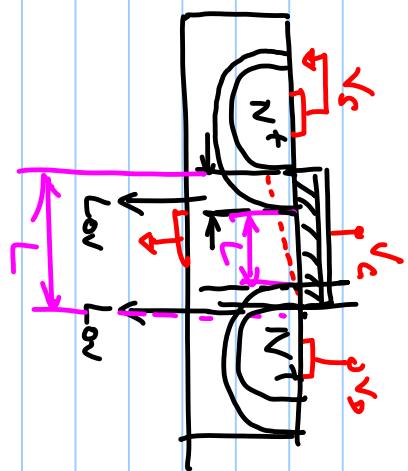




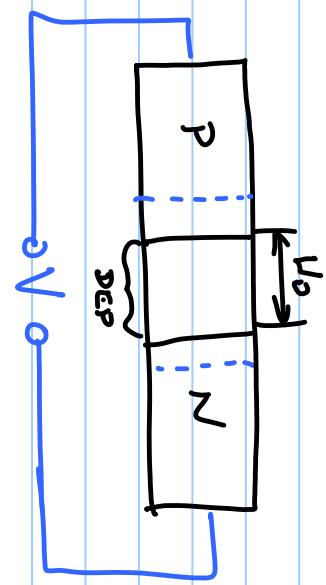
$$C_t = C_{GS} + C_{GD} + C_{DS} = \underline{C_{ox} \cdot W \cdot L} \rightarrow \underline{C}$$

$$C_{ov} = C_{ox} \cdot W \cdot L_{ov}$$

$$\begin{aligned} \text{TOTAL GATE CAP} &= C_t + 2C_{ov} = (C_{ox} \cdot W \underline{L} + 2C_{ox} \cdot W \cdot L_{ov}) \\ &= W(C_{ox} \cdot L + 2C_{ox} \cdot L_{ov}) \end{aligned}$$



C<sub>DG</sub> & C<sub>SG</sub>



$$\sqrt{L_0} \Rightarrow C \downarrow$$