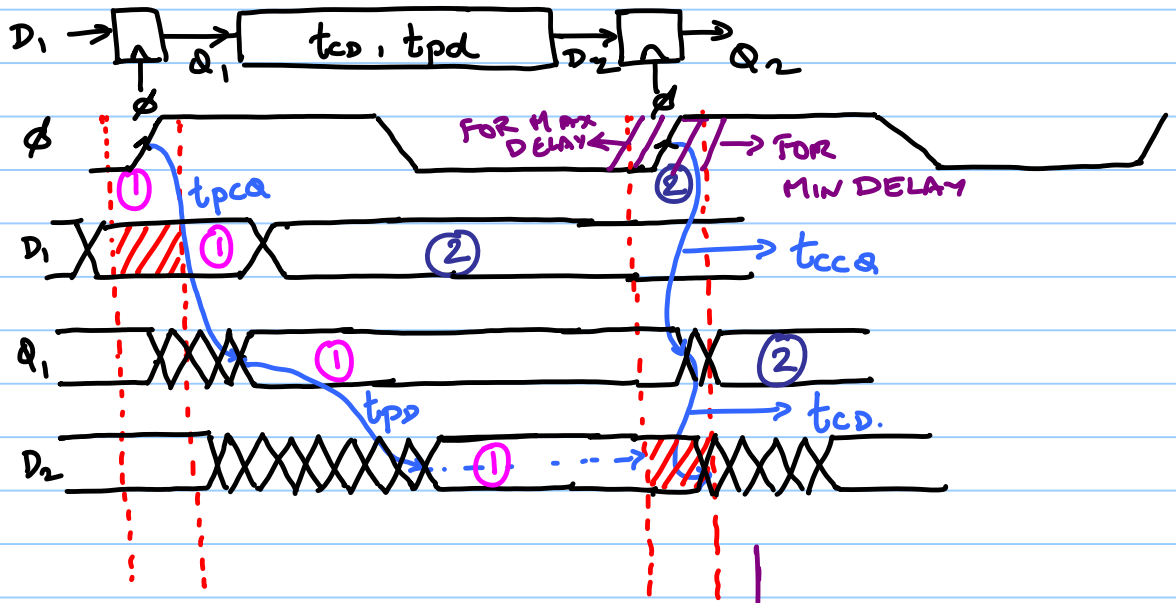
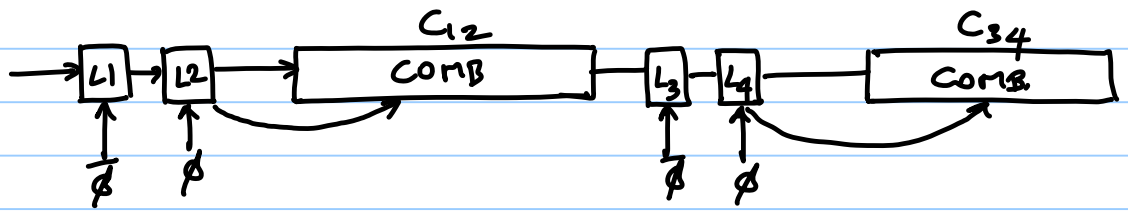
08/11/2019

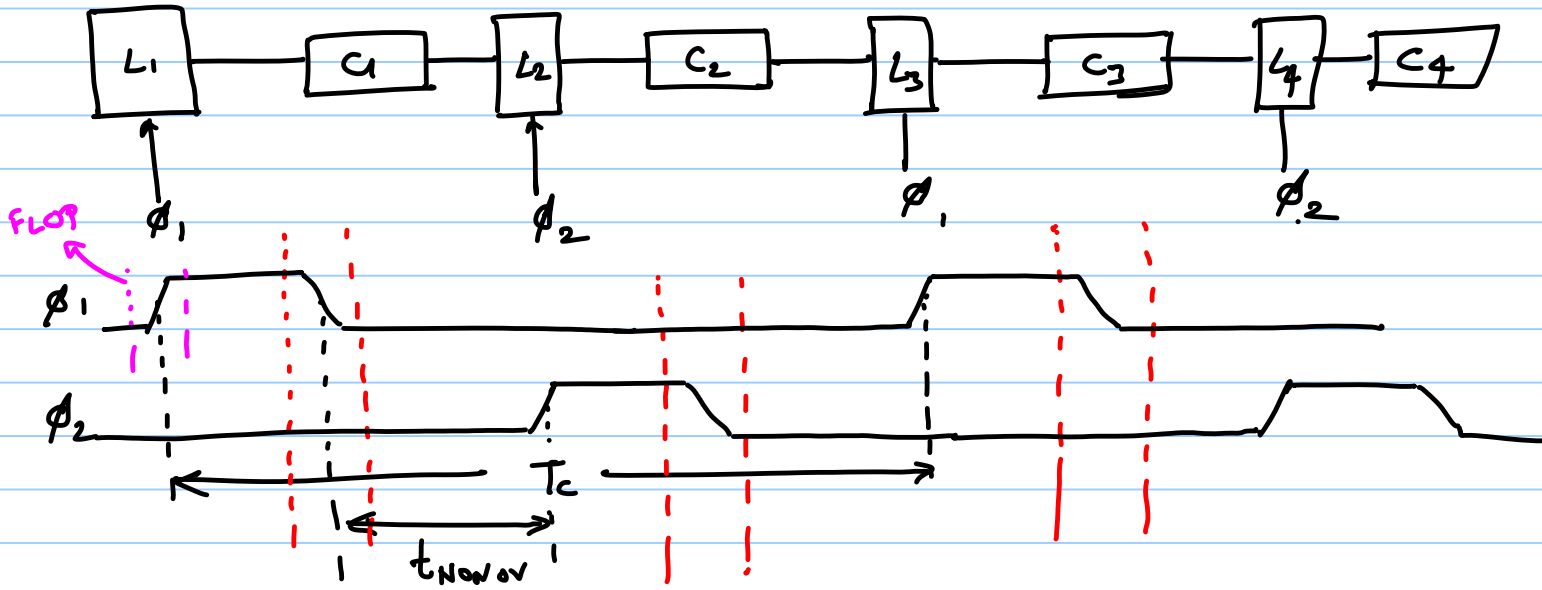
EE5311

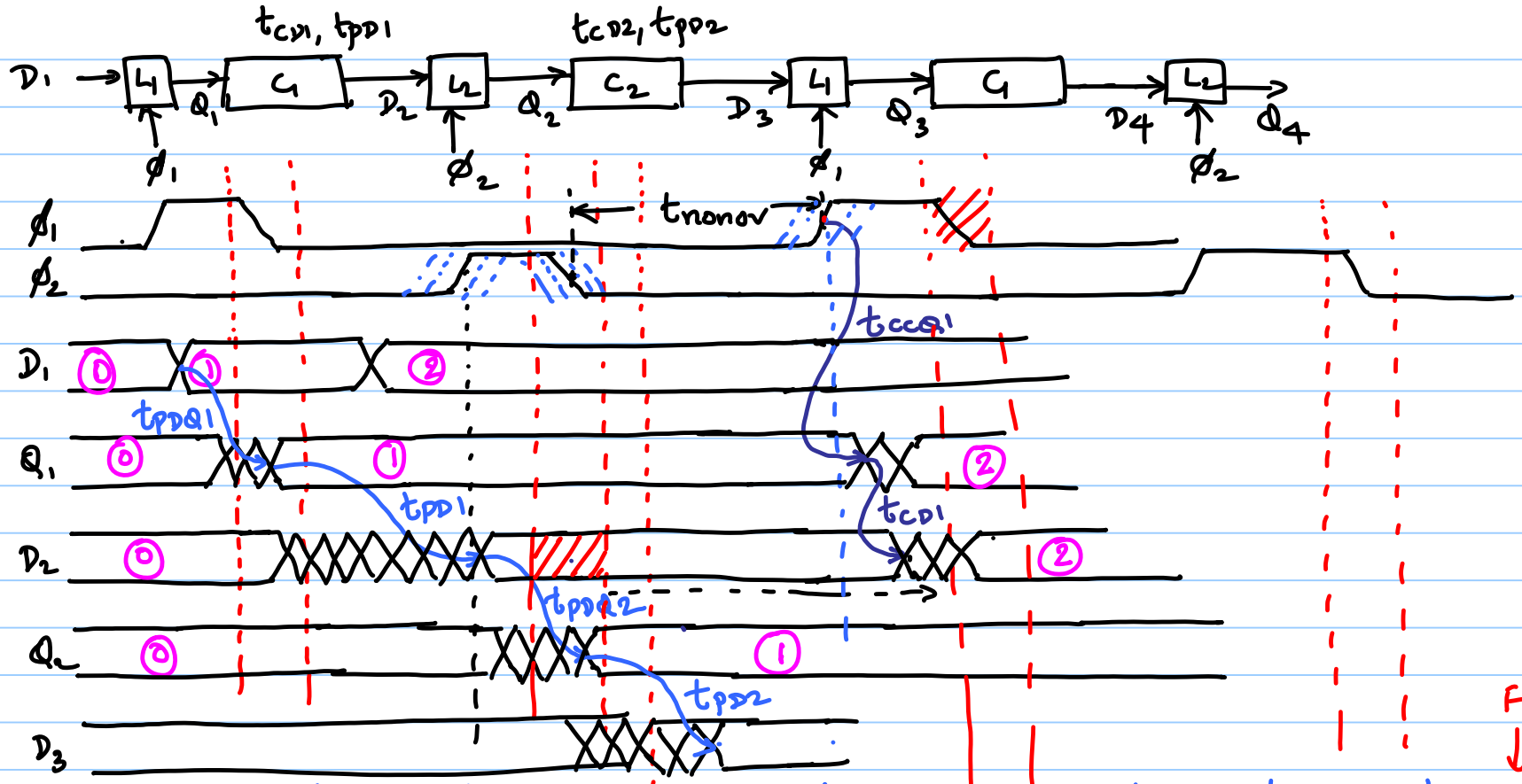
MODULE-5 SEQUENTIAL CIRCUITS



$T_c \geq t_{pCQ} + t_{pd} + t_{SETUP} + t_{SKEW}$ | $t_{SKEW} + t_{HOLD} \leq t_{CCQ} + t_{CD}$.


C_{12}
C_{34}
L1
L2
COMB
L3
L4
COMB
ϕ
ϕ
ϕ
ϕ


L_1
C_1
L_2
C_2
L_3
C_3
L_4
C_4
ϕ_1
ϕ_2
ϕ_1
ϕ_2
FLOP
ϕ_1
ϕ_2
T_c
t_{NONOV}

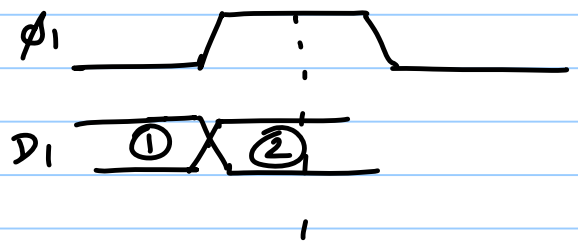


SKEW TOLERANT

MAX $T_C \geq t_{PDQ_1} + t_{PD1} + t_{PDQ_2} + t_{PD2}$

$t_{SKEW} + t_{HOLD} \leq t_{nonov} + t_{CCQ1} + t_{CD1}$

FOR EVERY LATCH


ϕ_1
D_1
1
2