

17/10/2019

EE5311

TUTORIAL - 3

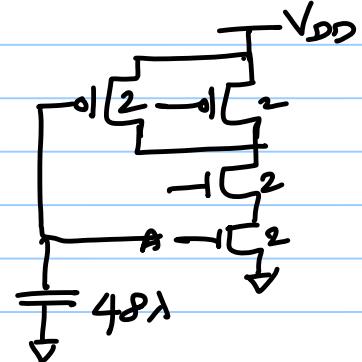
$$4KC = 8C$$
$$\therefore K = 2$$

$$x_4 = 48, 1$$

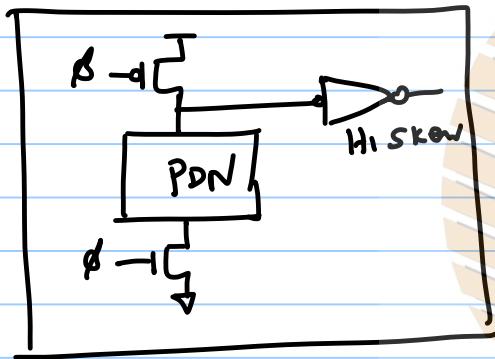
$$C_L = 2250 \lambda$$

$$5KC = PW_C$$

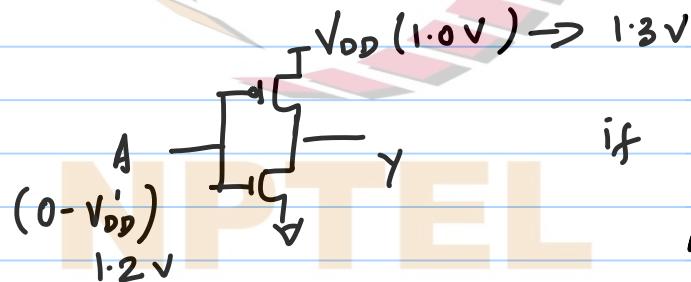
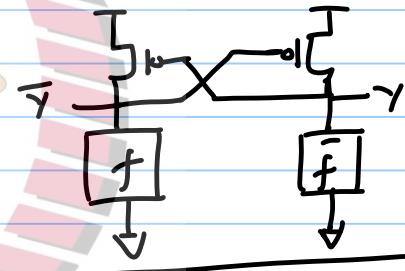
$\therefore K = 20$



MODULE 4:



CVSL

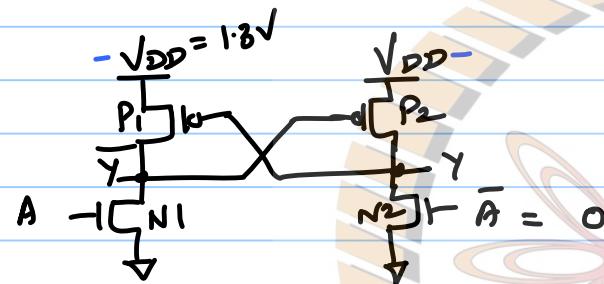


if $V_{DD}' > V_{DD}$

$$A = V_{DD}' = 1.2 \text{ V} \Rightarrow V_{GSP} = +0.2 \text{ V}$$

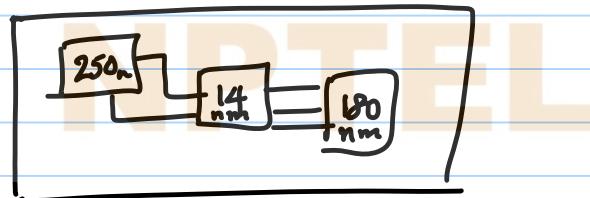
$$\text{if } V_{DD}' = 0.8 \text{ V} \text{ & } A = V_{DD}' = 0.8 \text{ V}$$

$$V_{GSP} = -0.2 \text{ V}$$

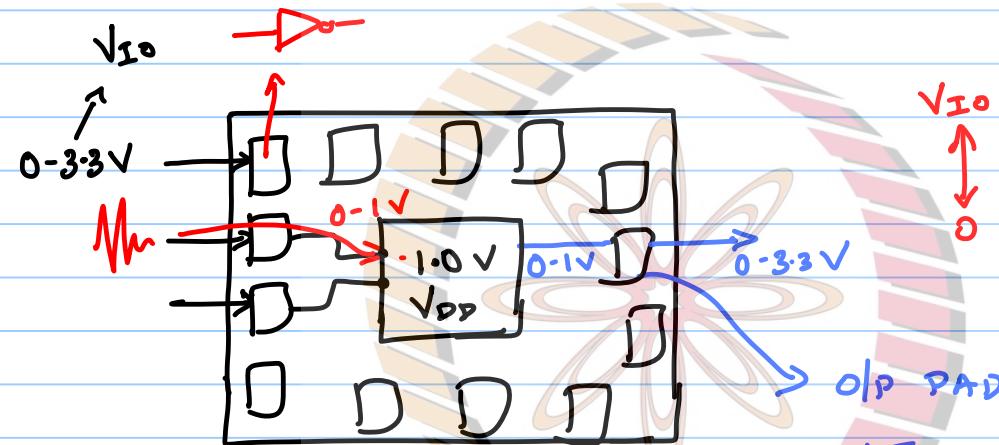


$A = 0.8V \Rightarrow N_1 \text{ is ON}$
 $\Rightarrow \text{GATE OF } P_2 = \text{LOW VOLTAGE}$
 $\Rightarrow P_2 \text{ TURN ON}$
 $\Rightarrow Y = \sqrt{DD} \quad (N_2 \text{ is OFF})$
 $\Rightarrow V_{GSP} \text{ OF } P_1 = 0$
 $\Rightarrow P_1 \text{ IS OFF}$

\Rightarrow CVELL IS GOOD FOR LEVEL TRANSLATION FROM LOW TO HIGH
 \sqrt{DD} DOMAIN



3.3 ✓



$V_{DD} = 1V$
THICK OXIDE
DEVICES
 $t_{ox} \sim 3\text{ nm}$
FOR THIN OXIDE
 $t_{ox} \sim 1\text{ nm}$
in 14 nm tech

