

Unit 3 - Week 1

Course outline

How to access the portal

Pre-Requisite Assignment

Week 1

Introduction: Objectives and Pre-requisites

Review of digital logic

Timing and Power in digital circuits

Implementation Costs and Metrics

Example: Audio processing

Example: AlexNet

Architecture cost components

Examples of Architectures

Multi-objective Optimization

Number representation

Scientific notation and Floating point

Quiz : Assignment 1

Week 1 Feedback : Mapping Signal Processing Algorithms to Architectures

Week 2

Week 3

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Week 5

Week 6

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Assignment 1

The due date for submitting this assignment has passed. **Due on 2019-08-14, 23:59 IST.**
As per our records you have not submitted this assignment.

You are asked to perform edge detection on a Full-HD video stream: 1920x1080 pixels at 60 frames per second, 32 bits per pixel.

1) What is the data rate (B/s) at which you are getting data into the system?

No, the answer is incorrect.
Score: 0

Accepted Answers:
(Type: Numeric) 497664000

1 point

2) You are performing edge detection by convolving a 3x3 filter to the pixels: each weight in the filter is a floating point number, and each pixel (32 bit) consists of 4 colours (RGB and alpha) that can be converted to floating point before computation. How many floating point multiply operations will you require per second?

Assume each colour has to be convolved separately and the output image size is kept the same as input by padding the border pixels if needed .

No, the answer is incorrect.
Score: 0

Accepted Answers:
(Type: Numeric) 4478976000

1 point

For each question below, select True (T) or False (F). Assume all other operating conditions are unchanged

3) Higher operating voltage will make a circuit operate more slowly.

True

False

No, the answer is incorrect.
Score: 0

Accepted Answers:
False

1 point

4) Higher operating voltage will make a circuit consume more power

True

False

No, the answer is incorrect.
Score: 0

Accepted Answers:
True

1 point

5) A larger gate will have a lower input capacitance than a smaller one.

True

False

No, the answer is incorrect.
Score: 0

Accepted Answers:
False

6) If we want to represent numbers having a dynamic range of 10^4 using two's complement binary numbers, the minimum number of bits required is

No, the answer is incorrect.
Score: 0

Accepted Answers:
(Type: Numeric) 15

1 point

A system designer finds several design solutions to a problem, and has 3 cost metrics: (area, latency, power). For all metrics, lower values are better. From the solutions given here, identify which are the Pareto optimal solutions, and briefly explain why the others are not. Solutions – $A : (10, 20, 50)$, $B : (20, 20, 50)$, $C : (12, 25, 60)$, $D : (8, 25, 30)$, $E : (20, 15, 40)$, $F : (30, 15, 50)$

7) Mark all the **Pareto Optimal** solutions here

A

B

C

D

E

F

No, the answer is incorrect.
Score: 0

Accepted Answers:
A
D
E
F

1 point

8) Mark all the solutions that are dominated by solution **A**

B

C

D

E

F

No, the answer is incorrect.
Score: 0

Accepted Answers:
B
C

1 point

9) Mark all the solutions that dominate solution **B**

A

C

D

E

F

No, the answer is incorrect.
Score: 0

Accepted Answers:
A
E

1 point

10) If only latency and power are considered important metrics, mark all the **Pareto Optimal** solutions here

A

B

C

D

E

F

No, the answer is incorrect.
Score: 0

Accepted Answers:
D
E

1 point