

## Unit 14 - Week 12

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## Assignment 12

The due date for submitting this assignment has passed.  
As per our records you have not submitted this assignment.

Due on 2019-10-23, 23:59 IST.

- 1) The interrupt in an 8085 microprocessor that is not affected by the value of the Interrupt Enable (IE) flip flop is
- INTR
  - RST 7.5
  - TRAP
  - RST 5.5
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: c.
- 2) Which of the following statements regarding 8085 interrupts is FALSE?
- An interrupt is a process that starts from an I/O device and is asynchronous.
  - An interrupt can either be a maskable or a non-maskable one.
  - An interrupt can either be a vectored or a non-vectored one.
  - An interrupt is serviced only after the microprocessor completes the execution of its current program.
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: d.
- 3) Which of the following statements regarding CALL and RET instructions is FALSE?
- An 8085 program may contain multiple RET instructions
  - In an 8085 program, the first CALL instruction may appear before the first RET instruction
  - In an 8085 program, the first RET instruction may appear before the first CALL instruction
  - An 8085 program may contain multiple CALL instructions
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: c.
- 4) Which of the following statements about the stack in an 8085 microprocessor is FALSE?
- Stack is a last-in-first-out structure.
  - Size of the register associated with the stack is 8 bits.
  - Information is saved on the stack by pushing on it.
  - Information is retrieved on the stack by popping it off.
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: b.
- 5) With respect to an 8085 microprocessor, match column X with column Y.
- | Column X   | Column Y        |
|------------|-----------------|
| 1. INTR    | 1. Non-maskable |
| 2. RST 5.5 | 2. Maskable     |
| 3. TRAP    | 3. Software     |
| 4. RST 1   | 4. Non-vectored |
- X1-Y2, X2-Y1, X3-Y4, X4-Y3
  - X1-Y4, X2-Y2, X3-Y1, X4-Y3
  - X1-Y3, X2-Y2, X3-Y4, X4-Y1
  - X1-Y1, X2-Y4, X3-Y2, X4-Y3
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: b.
- 6) Through which pin external DMA controller sends a control signal to an 8085 microprocessor?
- HOLD
  - HLDA
  - INTR
  - INTA
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: a.
- 7) Which of these is not an error checking method?
- Parity Check
  - Checksum
  - Cyclic redundancy check
  - Huffman code
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: d.
- 8) After the execution of instruction RIM, the accumulator contained 49H. Which of the following statement is TRUE in this case?
- RST 5.5 is enabled and is pending.
  - RST 6.5 is enabled and is pending.
  - RST 7.5 is enabled and is pending.
  - None of the given options.
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: c.
- 9) The READY pin of 8085 is used
- By the microprocessor to tell the device when it is ready
  - By the devices to tell the microprocessor when it is ready
  - For input/output operations
  - All of the other three options
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: b.
- 10) In 8086, the address bus is \_\_\_\_\_ bits wide.
- 10
  - 12
  - 16
  - 20
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: d.
- 11) Which of the following pins in an 8086 microprocessor are multiplexed with data and address signals?
- AD<sub>15</sub> – AD<sub>0</sub>
  - AD<sub>19</sub> – AD<sub>0</sub>
  - AD<sub>19</sub> – AD<sub>1</sub>
  - AD<sub>19</sub> – AD<sub>4</sub>
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: a.
- 12) Which of the following is NOT a data transfer instruction in an 8086 microprocessor?
- MOV
  - XCHG
  - SBB
  - PUSH
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: c.
- 13) Which of the following is NOT a valid 8085 instruction?
- POP PSW
  - POP B
  - POP C
  - POP D
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: c.
- 14) With respect to 8085, match Column X with Column Y.
- | Column X   | Column Y                    |
|------------|-----------------------------|
| 1. RST 5.5 | 1. Edge-triggered           |
| 2. RST 6   | 2. Level-triggered          |
| 3. RST 7.5 | 3. Edge and level triggered |
| 4. RST 4.5 | 4. Software                 |
- X1-Y3, X2-Y1, X3-Y4, X4-Y2
  - X1-Y4, X2-Y2, X3-Y1, X4-Y3
  - X1-Y2, X2-Y4, X3-Y1, X4-Y3
  - X1-Y3, X2-Y4, X3-Y2, X4-Y1
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: c.
- 15) In 8085, the numbers 45H and 7EH are added. The resulting flag settings will be as follows.
- Both CY and AC are set
  - CY Set AC reset
  - CY reset AC set
  - Both CY and AC reset
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers: c.