Unit 9 - Week 7 : I/O Interfacing

Assignment 7

The due date for submitting this assignment has passed.
As per our records you have not submitted this assignment.

Due on 2020-03-18, 23:59 IST.

1) Which of the following operation is not involved in interfacing a keyboard to 8086?   *0 points*

○ Detect a keypress
○ Debounce the keypress
○ Decode the keypress
○ Encode the keypress

No, the answer is incorrect.
Score: 0
Accepted Answers:
Decode the keypress

2) The process of making sure a previous key has been released before looking for next one is called   *1 point*

○ Decoding of the key
○ Debouncing of the code
○ Encoding of the key
○ Two-key lockout

No, the answer is incorrect.
Score: 0
Accepted Answers:
Two-key lockout

3) In order to interface a hexadecimal keyboard to 8086   *1 point*

○ One 8-bit port of 8255 must be programmed as input port and 4-bits of another port must be programmed as output port
○ One 8-bit port of 8255 must be programmed as output port and 4-bits of another port must be programmed as input port
○ One 8-bit port of 8255 and 4-bits of another port must be programmed as output ports
○ One 8-bit port of 8255 and 4-bits of another port must be programmed as input port

No, the answer is incorrect.
Score: 0
Accepted Answers:
One 8-bit port of 8255 must be programmed as input port and 4-bits of another port must be programmed as output port

4) Let a BCD to 7-segment decoder (7447) is interfaced to a common anode type 7-segment display. If the BCD input to 7447 is 1001 and common signal of 7-segment display is connected to logic 1   *1 point*

○ All the segments of display will be at logic 0
○ Only the segment "e" will be at logic 0
○ All the segments except "e" will be at logic 0
○ All the segments except "e" will be at logic 1

No, the answer is incorrect.
Score: 0
Accepted Answers:
All the segments except "e" will be at logic 0

5) In order to drive eight 7-segment displays, the worst case current required would be   *1 point*

○ 140 mA
○ 160 mA
○ 980 mA
○ 1120 mA

No, the answer is incorrect.
Score: 0
Accepted Answers:
1120 mA

6) Which of the following circuit is preferred to drive relay coil?   *1 point*

○ One PNP transistor buffer
○ One NPN transistor buffer
○ Darlington transistor
○ IC buffer

No, the answer is incorrect.
Score: 0
Accepted Answers:
Darlington transistor

7) Which of the following circuit is preferred to drive motor winding?   *1 point*

○ One PNP transistor buffer
○ One NPN transistor buffer
○ Darlington transistor
○ Power MOSFET circuit

No, the answer is incorrect.
Score: 0
Accepted Answers:
Power MOSFET circuit

8) Which of the following circuit is not involved in the internal circuitry of solid-state relay?   *1 point*

○ Phototransistor
○ Rectifier
○ Zero crossing detector
○ Trigger circuit

No, the answer is incorrect.
Score: 0
Accepted Answers:
Rectifier