Announcements



NPTEI

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Course

Courses » Computer Organization and Architecture

Unit 4 - Week 3 Week3 Assignment Course outline How to access the portal Week 3 Assignment Week 1 1) The addressing modes involved in the following instruction are 1 point Week 2 MOV EAX, 0x10 Week 3 Register, Register Register , Memory Lecture 07 Part Register, Immediate Programming Immediate, Memory using X86 ISA -Addressing Modes Lecture 07 Part **Accepted Answers:** Register, Immediate Programming using X86 ISA -2) What does the following instruction result in? 1 point Addressing **DIV EAX** Modes Compiler Error Lecture 08 Part 1 - Floating Syntax Error point - Precision Divides EAX by 1 and Accuracy Divides EAX by EAX Lecture 08 Part 2 - Floating Point - Addition, Subtraction and **Accepted Answers:** Multiplication Divides EAX by EAX Lecture 09 -Instruction Set 3) The remainder of the DIV operation is stored in which of the following registers? 1 point Architecture EAX Ouiz: Week3 EBX Assignment ECX Lab Exercise -1 EDX Feedback for week 3 Week 3 Assignment **Accepted Answers:** Solutions **EDX** Week 4 4) Which of the following characterizes the memory in x86 architecture? 1 point

Week 5

bit-addressible

byte-addressible

Computer Organization and Architecture - - Unit 4 - Week 3

| Week 6 | O long-addressible | |
|---------|--|---------|
| Week 7 | odouble-addressible | |
| | | |
| Week 8 | Accepted Answers: | |
| Week 9 | byte-addressible | |
| Week 10 | 5) Which of the following characterizes the x86 architecture? | 1 point |
| Week 11 | Simple Instruction Set Architecture Reduced Instruction Set Architecture | |
| | Extended Instruction Set Architecture | |
| Week 12 | Complex Instruction Set Architecture | |
| | Accepted Answers: | |
| | Complex Instruction Set Architecture | |
| | 6) PF, the parity flag checks for which of these? | 1 point |
| | If number of 1's in the bit is even | |
| | If the number of 1's is greater than number of 0's | |
| | If all the bits are 0 | |
| | If all the bits are 1 | |
| | Accepted Answers: | |
| | If number of 1's in the bit is even | |
| | 7) ADD AL, [0x1000] is a | 1 point |
| | 4-bit instruction | |
| | 8-bit instruction | |
| | 16-bit instruction | |
| | 32-bit instruction | |
| | Accepted Answers: 8-bit instruction | |
| | 8) Which of the following is the correct definition of accuracy? | 1 point |
| | How is the measured value to the true value? | |
| | How close are results from different experiments? | |
| | Accepted Answers: How is the measured value to the true value? | |
| | 9) In 32-bit IEEE 754 floating point format, the exponents are stored in which of the following formats? | 1 point |
| | decimal | |
| | excess-127 | |
| | excess-64 | |
| | exponent as integer | |
| | Accepted Answers: | |

| excess-127 | |
|--|---------|
| 10)What kind of processor is x86? | 1 point |
| RISC CISC | |
| DISCNone of the above | |
| Accepted Answers: CISC | |
| | |

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