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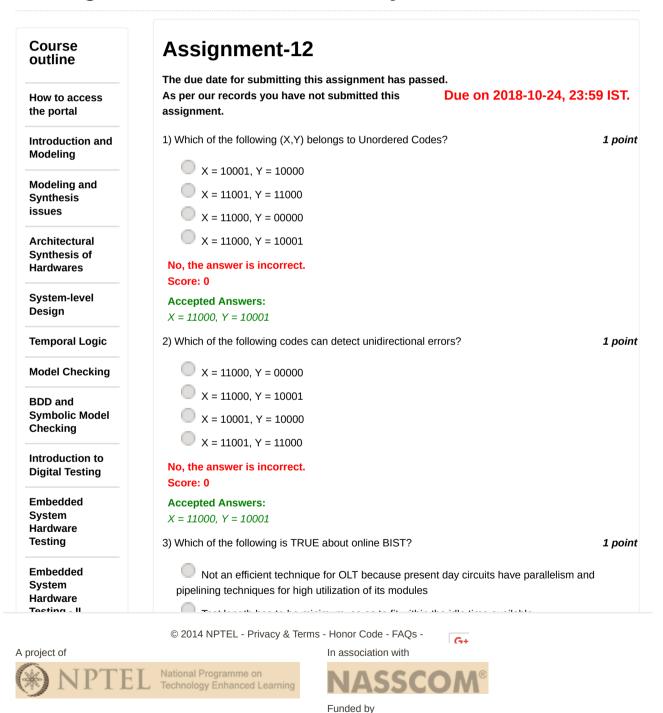
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## Unit 14 - Testing for Embedded Software Systems



Advances in Embedded System Hardware Testing - II  Testing for Embedded Software Systems  Testing for Reprogrammable hardware	All of the above  4) A can be configured to realize any possible combinational output for a given number 1 point of inputs.  CLB  IOB  LUT  None of the above  No, the answer is incorrect. Score: 0  Accepted Answers:
Testing between Hardware and Software  Quiz: Assignment-12	5) Which of the following is TRUE about Virtual Machines?  Developer can treat it as the real machine  A software layer very close to the hardware that hides the hardware's details and provides an abstract view to the programmer  Operating systems are often viewed as virtual machines
	No, the answer is incorrect. Score: 0 Accepted Answers: All of the above
	6) Which of the following is correct about the current software/hardware design process?  1 point  Hardware problems can be fixed with simple software modifications  Once operational, software rarely needs modification or maintenance  Valid and complete software requirements are easy to state and implement in code  Hardware and software cannot be acquired separately and independently, with their successful and easy integration
	No, the answer is incorrect. Score: 0 Accepted Answers: Hardware and software cannot be acquired separately and independently, with their successful and easintegration
	7) Which of the following is TRUE about the duplication schemes for online testing?  The circuit duplication is for crosschecking the output responses for Similarity In identical duplication, circuits are structurally equivalent In partial duplication, a minimized version of the circuit under test is replicated.  All of the above  No, the answer is incorrect.  Score: 0  Accepted Answers:
	All of the above  8) Which of the following is TRUE about online testing techniques?  1 point  Self checking design using error detecting codes is non-intrusive

The hardware overhead for duplication schemes for On-line Testing does not exceed 1	.00%
On-line BIST depends on the idle time of the modules	
All of the above	
No, the answer is incorrect.	
Score: 0	
Accepted Answers:	
On-line BIST depends on the idle time of the modules	
9) The components to be tested in FPGA testing are:	1 point
CLB	
I/O Pins	
Interconnect Networks	
All of the above	
No, the answer is incorrect.	
Score: 0	
Accepted Answers:	
All of the above	
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