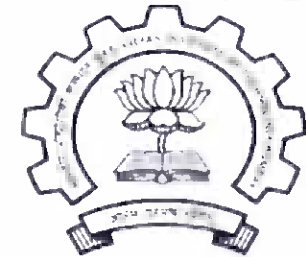
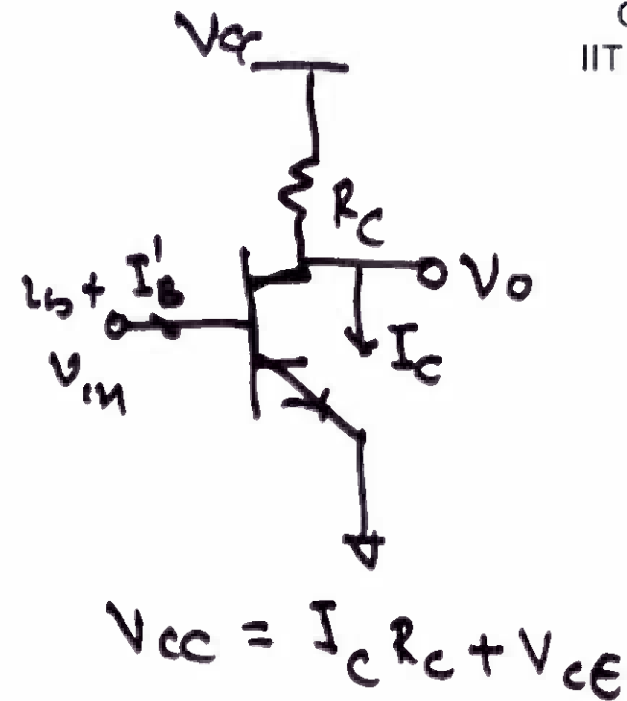
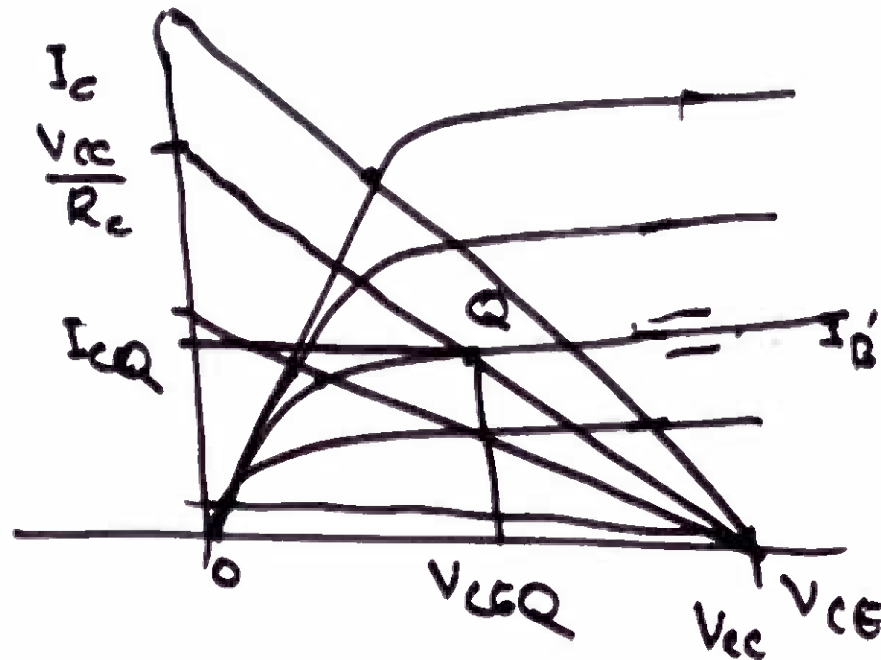


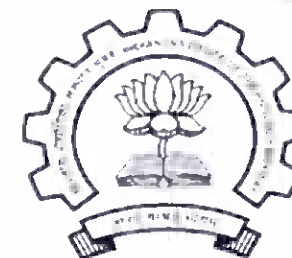
Slide No: 1

Biassing for a BJT Amplifier



CDEEP
IIT Bombay

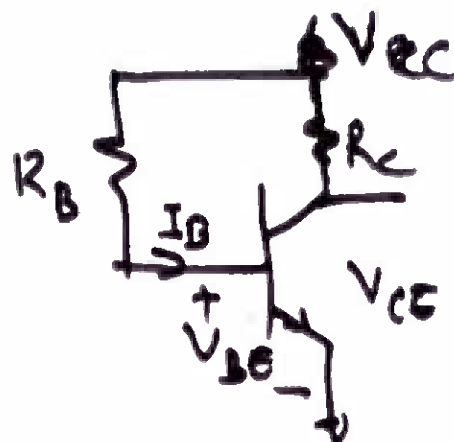




CDEEP
IIT Bombay

Slide No: 2

BJT Biasing



$$V_{CC} = 12V$$

$$\beta = 100$$

$$V_{BE_{on}} = 0.7V$$

$$\begin{cases} V_{CEQ} = 6V \\ I_{CQ} = 1mA \end{cases}$$

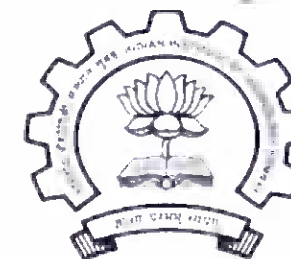
$$I_{CQ} = 1mA$$

$$V_{CE} = V_{CC} - I_C R_C \quad - (1)$$

$$I_{BQ} = \frac{1mA}{100} = 10\mu A$$

$$V_{CC} = I_B \cdot R_B + V_{BE_{on}} \quad - (2)$$

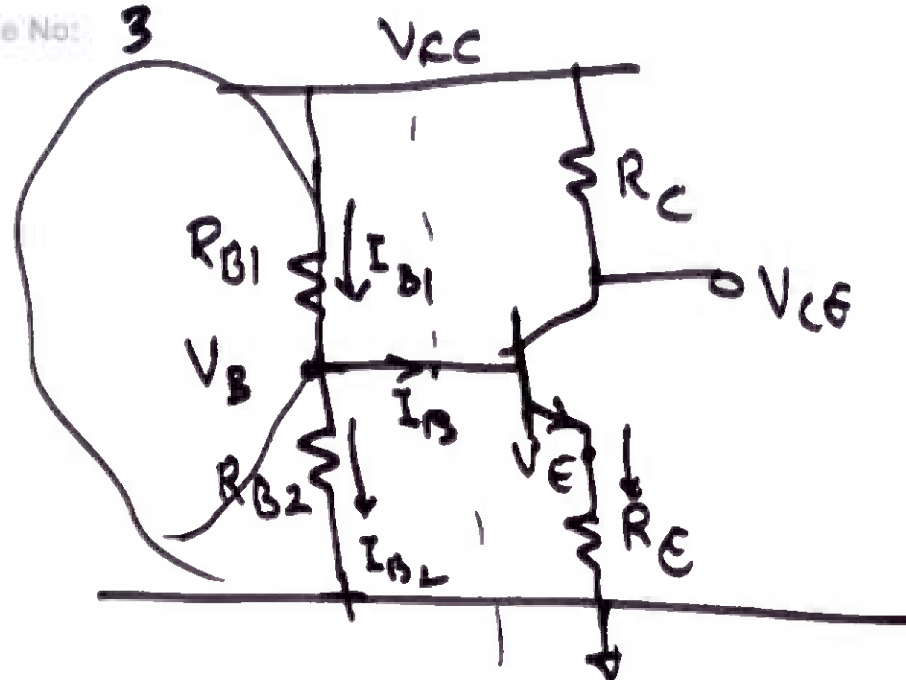
$$R_C = 6k\Omega \quad R_B \leq 1.1M\Omega$$



CDEEP
IIT Bombay

Slide No:

3



$$I_{B1} = I_{B2} + I_B$$

$$V_{CC} = I_C R_C + V_{CE} + I_E R_E$$

$$I_E = I_B + I_C$$

$$I_E = (\beta + 1) I_B$$

$$I_C = \beta I_B$$

$$I_E R_E + V_{BE0} = V_B$$

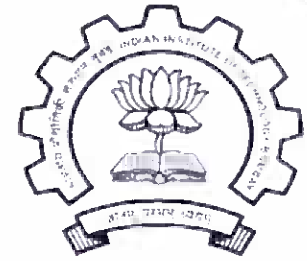
Course Name

Analog Circuits

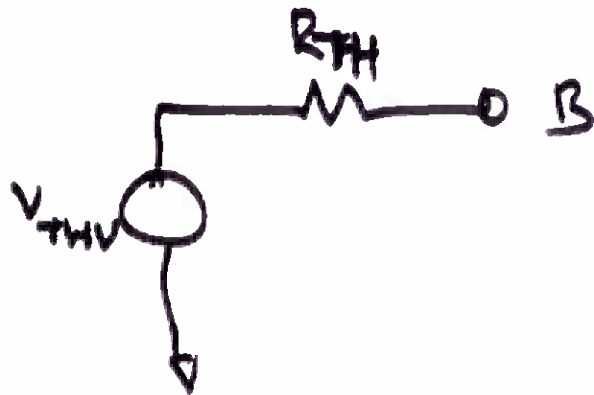
Lecture No. 6

Instructor's Name

Prof. A. N. Chandorkar

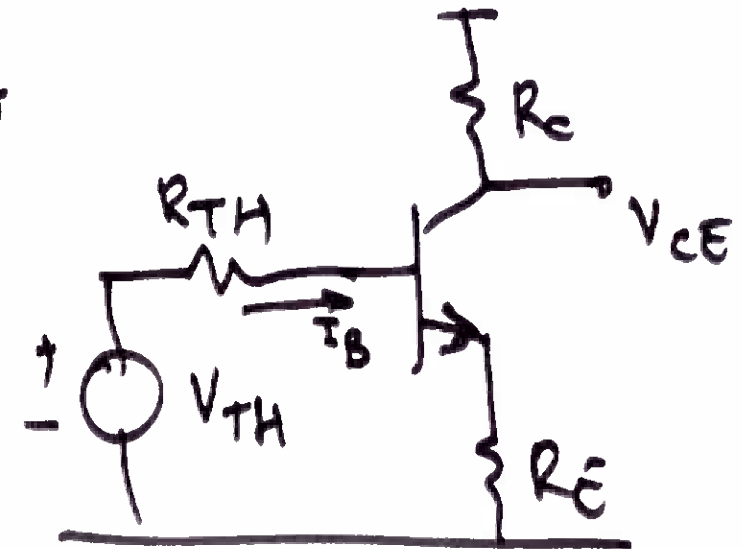


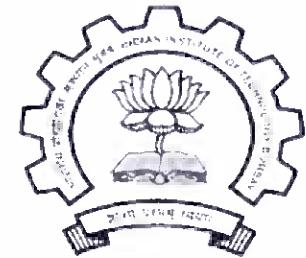
CDEEP
IIT Bombay



$$V_{TH} = \frac{R_{B2}}{R_{B1} + R_{B2}} \cdot V_{CC}$$

$$R_{TH} = R_{B1} \parallel R_{B2}$$





$$V_{THQ} = 1.79 = I_{BQ} R_{TH} + V_{BEon} + I_{EQ} R_E$$

$$I_{EQ} = (\beta_0 + 1) I_{BQ}$$

$$I_{BQ} = 21.6 \mu A$$

$$I_{CQ} = 100 \times 21.6 \mu A = 2.16 \text{ mA}$$

$$I_{EQ} = 101 \times 21.6 \mu A = 2.18 \text{ mA}$$

$$V_{CEQ} = V_{CC} - (I_{CEQ} R_C + I_{EQ} R_E) = 4.8 \text{ V}$$

$$I_{CQ} = 2.16 \text{ mA}$$

$$V_{CEQ} = 4.8 \text{ V}$$

Slide No: 6

$$R_{B1} = 56K \quad R_{B2} = 12.2K$$

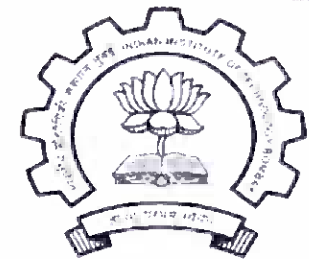
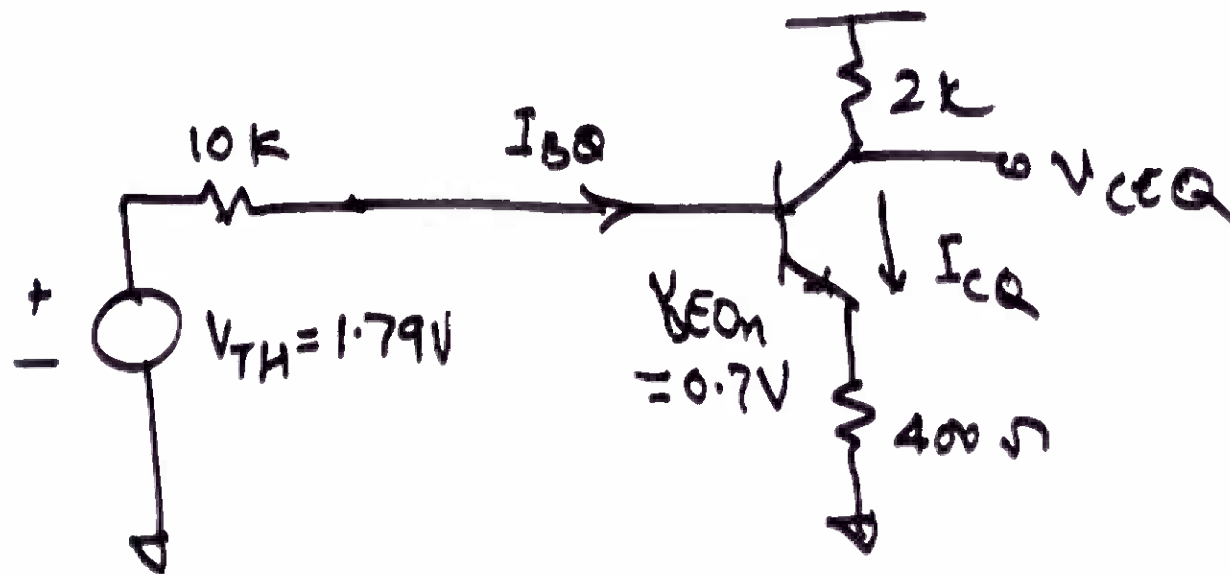
$$R_C = 2K \quad R_E = 400\Omega \quad \beta = 100$$

$$V_{CC} = 10V$$

$$V_{BE(on)} = 0.7V$$

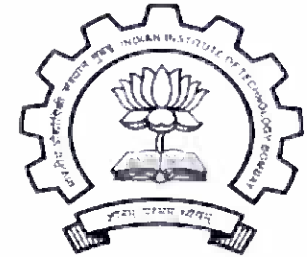
$$V_{TH} = 1.79V$$

$$R_{TH} = 10K$$

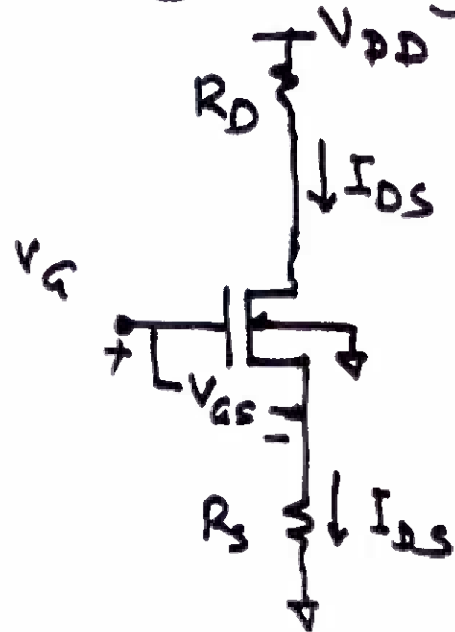


CDEEP
IIT Bombay

MOSFET Amplifiers.

CDEEP
IIT Bombay

① Biasing (DC operation)



Clearly $V_G = V_{GS} + I_{DS} R_S$

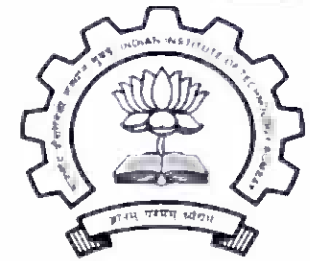
However $I_{DS} = \frac{\beta}{2} (V_{GS} - V_T)^2$
 $= \frac{\beta}{2} V_{OV}^2$

Assume
[$\lambda=0$]

But $V_{GS} = V_G - I_{DS} R_S = V_G - \frac{\beta}{2} V_{OV}^2 \cdot R_S$

Hence fixing V_G , we can fix V_{GS} .

R_S is called Degeneration resistance



Slide No: 8

Example from (SMC)

Let $\beta_n = 1 \text{ mA/V}^2$ $I_{D_S} = \frac{1}{2} \text{ mA}$

$\lambda = 0$ $V_{DD} = 15 \text{ V}$ $V_T = 1.5 \text{ V}$

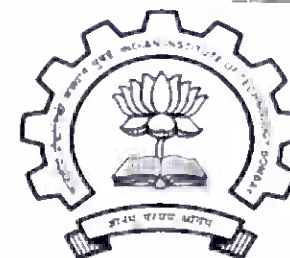
$R_D = 10 \text{ k}\Omega$ $R_S = 10 \text{ k}\Omega = 10$

$$\therefore V_{DS} = V_{DD} - I_{D_S} (R_D + R_S)$$

$$= 15 - 0.5 (20 \times 10^3) 10^{-3} = 15 - 10 = 5 \text{ V}$$

For Saturation $V_{DS} > V_{GS} - V_T$

$$\text{or } 5 + 1 > V_{GS} \quad \text{or } V_{GS} < 6 \text{ V}$$



But $V_G = V_{GS} + I_{DS} R_S$

$$= V_{GS} + 0.5 \times 10^{-3} \times 10 \times 10^3$$
$$= V_{GS} + 5$$

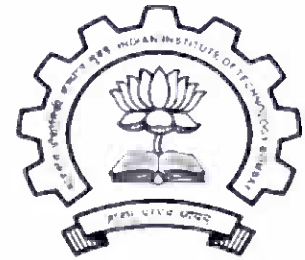
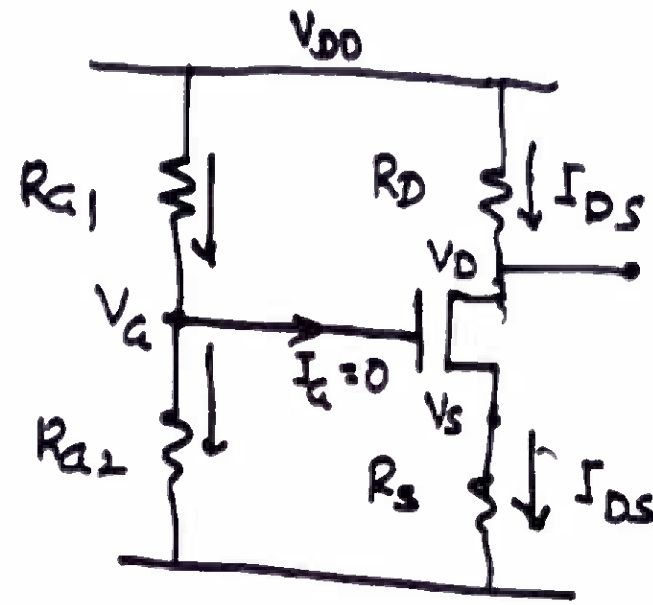
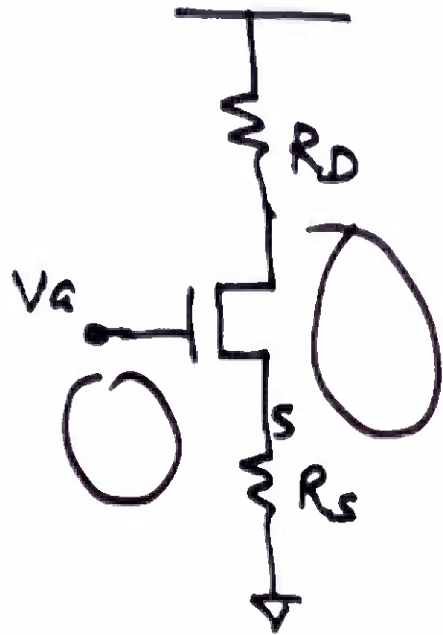
We have $I_{DS} = 0.5 \times 10^{-3} = \frac{\beta_n}{2} (V_{GS} - V_T)^2$

$$\text{or } \sqrt{\frac{2 \times 0.5 \times 10^{-3}}{1 \times 10^{-3}}} + 1 \text{ V} = V_{GS}$$

$$\text{or } V_{GS} = 2 \text{ V}$$

$$\therefore V_G = 2 + 5 = 7 \text{ V}$$

Amplifier (Common Source)

CDEEP
IIT Bombay

$$R_D = \frac{V_{DD} - V_D}{I_{DS}} ; \quad R_S = \frac{V_S}{I_{DS}}$$

$$I_{DS} = \frac{\beta_n}{2} [V_{GS} - V_T]^2 = \frac{\beta_n}{2} V_{OV}^2$$



$$V_E = \frac{R_{G2}}{R_{G1} + R_{G2}} \cdot V_{DD}$$

$$\text{or } 7 = \frac{R_{G2}}{R_{G1} + R_{G2}} \cdot 15$$

$$\text{or } 7(R_{G1} + R_{G2}) = 15 R_{G2}$$

$$\text{or } 8 R_{G2} = 7 R_{G1}$$

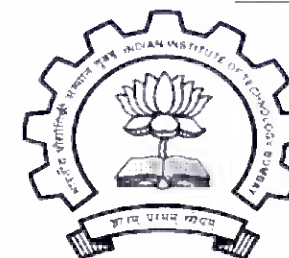
$$\text{Assume } R_{G1} = 7 \text{ M}\Omega$$

$$\text{Then } R_{G2} = \frac{49}{8} \text{ M}\Omega$$

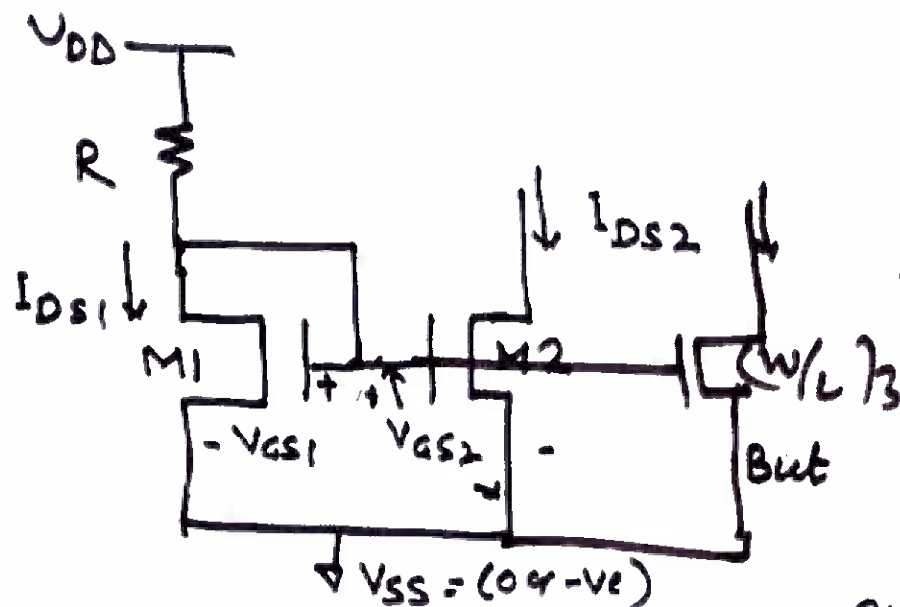
Slide No: 12

Current Source Biasing

Current Mirrors



CDEEP
IIT Bombay

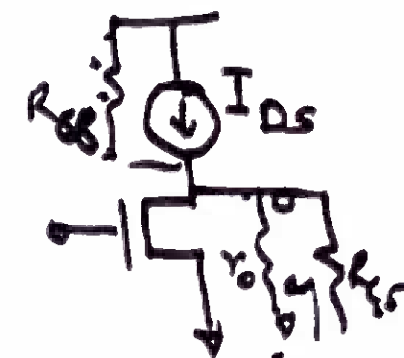


$$I_{DS1} = \frac{V_{DD} - V_{GS1} - V_{SS}}{R}$$

$$\underline{V_{GS1}} = \underline{V_{GS2}}$$

$$\text{and } \underline{V_{DS1}} = \underline{V_{GS1}}$$

Since $V_{DS1} > V_{GS1} - V_T$ M1 is in Saturation



Slide No: 13

From Transistor I-V

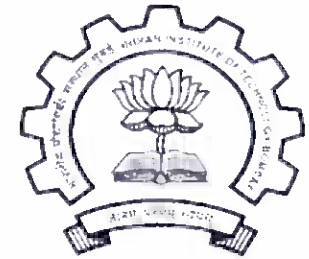
$$I_{Ds1} = \beta'_{n1} \left(\frac{W}{L}\right)_1 (V_{Gs1} - V_{Tn})^2 \quad [\lambda=0]$$

$$I_{Ds2} = \beta'_{n2} \left(\frac{W}{L}\right)_2 (V_{Gs2} - V_{Tn})^2$$

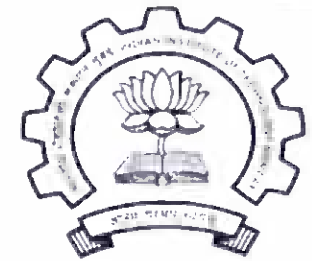
Since $\beta'_n = \mu_n C_{ox}$, common for both $M1$ & $M2$
and that $V_{Gs1} = V_{Gs2}$

$$\therefore \frac{I_{Ds2}}{I_{Ds1}} = \frac{(W/L)_2}{(W/L)_1}$$

$$\text{where } I_{Ds1} = I_{Ref} = \frac{V_{DD} - V_{Gs1} - V_{ss}}{R} = \beta_n \left(\frac{W}{L}\right)_1 (V_{Gs1} - V_{Tn})^2$$



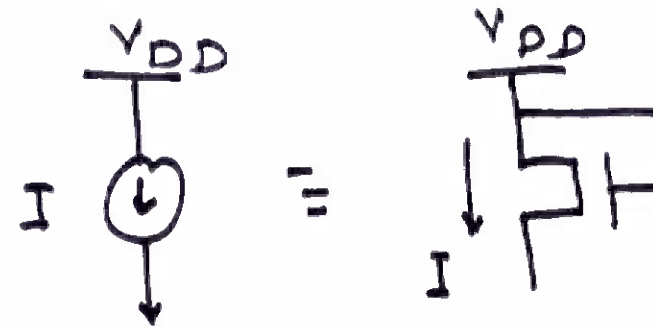
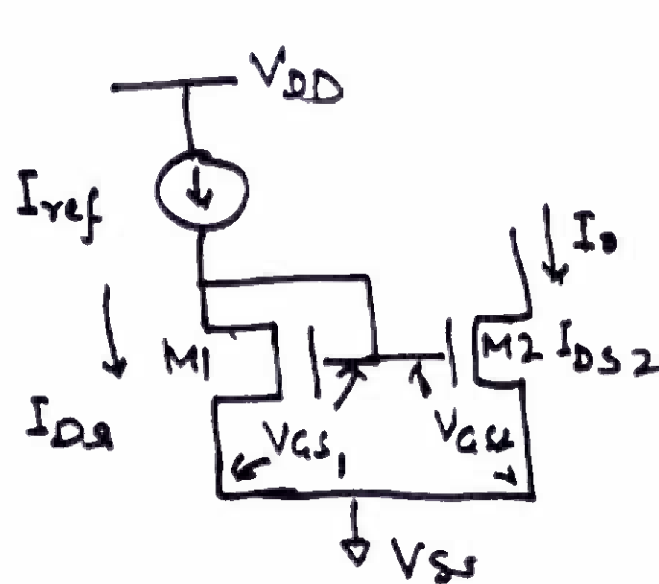
CDEEP
IIT Bombay



CDEEP
IIT Bombay

Slide No: 14

Other Current Sources & Mirrors



Design Current Source

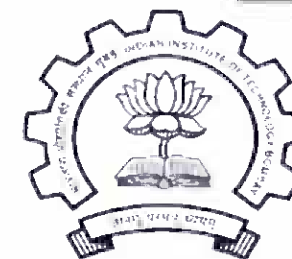
for $I_{ref} = 0.25 \text{ mA}$ & $I_O = 0.10 \text{ mA}$

Given $V_{DD} = 5 \text{ V}$ $V_{SS} = 0$ $V_{TH} = 1 \text{ V}$ $V_{OV} = 200 \text{ mV}$

$$\beta'_n = 40 \mu\text{A/V}^2$$

$(W/L)_1$ & $(W/L)_2$ are the ones we need to evaluate

Slide No: 15



CDEEP
IIT Bombay

$$I_{DS1} = I_{Ref} = \frac{1}{2} \beta'_n \left(\frac{W}{L}\right)_1 (V_{GS1} - V_T)^2$$

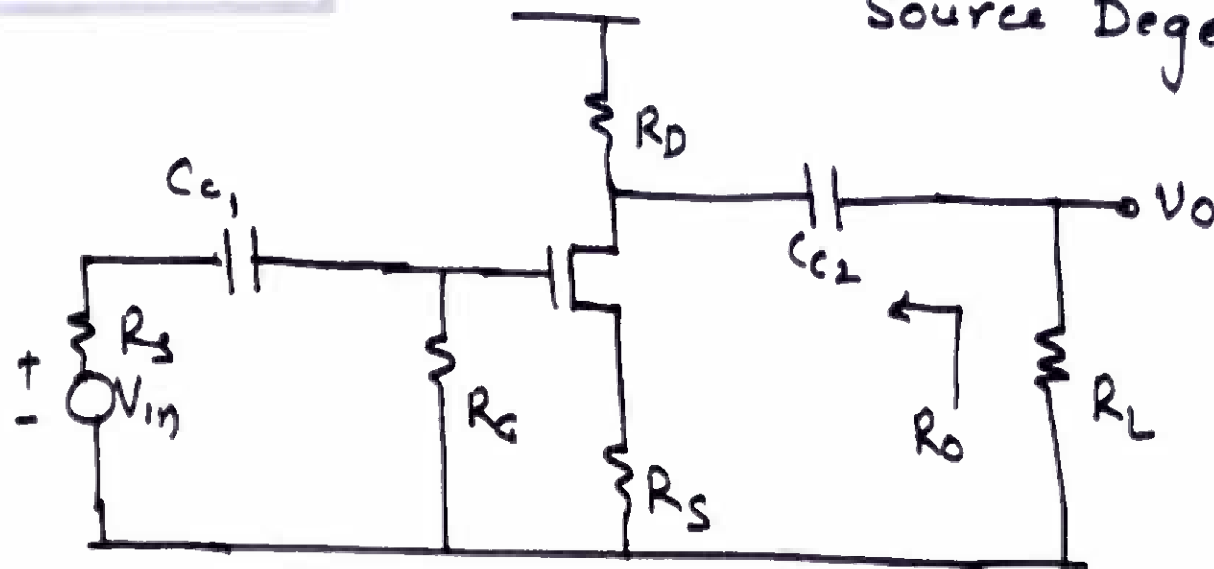
$$0.25 \times 10^{-3} = \frac{1}{2} \times 20 \left(\frac{W}{L}\right)_1 (200 \times 10^{-3})^2$$

$\therefore \left(\frac{W}{L}\right)_1$ is known

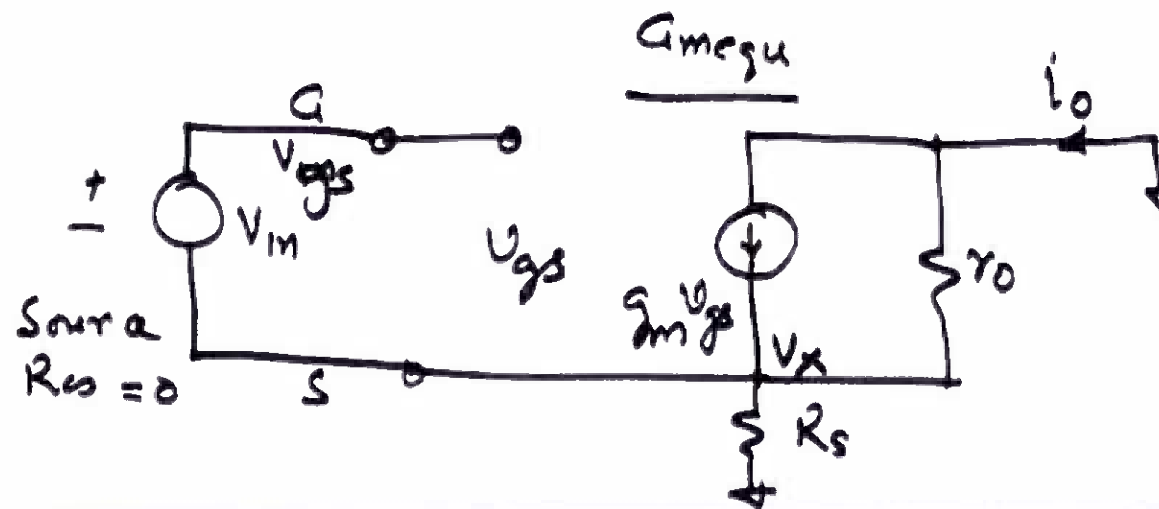
$$\text{Since } \frac{I_{DS2}}{I_{DS1}} = \frac{I_0}{I_{Ref}} = \frac{0.1}{0.25} = \frac{\left(\frac{W}{L}\right)_2}{\left(\frac{W}{L}\right)_1}$$

$$\therefore \left(\frac{W}{L}\right)_2 = \frac{2}{5} \left(\frac{W}{L}\right)_1$$

Common Source Amplifier with Source Degeneration

CDEEP
IIT Bombay

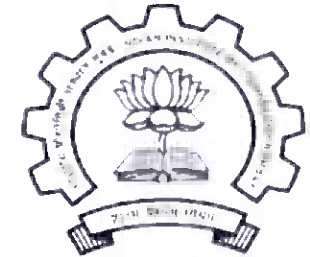
$$G_m = \frac{\partial I_{D1}}{\partial V_{gs}} = \frac{I_o}{V_{gs}}$$



$$I_o = g_m V_{gs} - \frac{V_x}{R_S}$$

$$V_x = I_o R_S$$

$$\therefore I_o = g_m V_{gs} - \frac{I_o R_S}{R_D}$$



CDEEP
IIT Bombay

Slide No: 17 $\therefore i_o = g_m (V_{in} - i_o R_s) - \frac{i_o R_s}{r_o}$

$$\therefore i_o \left[1 + g_m R_s + \frac{R_s}{r_o} \right] = g_m V_{in}$$

$$\therefore G_m = \frac{i_o}{V_{in}} = \frac{g_m r_o}{(1 + g_m R_s) r_o + R_s}$$

Then Voltage Gain $A_v = -G_m (R_D \parallel R_L)$

$$= - \frac{g_m (R_D \parallel R_L)}{(1 + g_m R_s) + R_s / r_o}$$

As
 $r_o \gg R_s$

If $R_s > \frac{1}{g_m}$ then $A_v = - \frac{g_m (R_D \parallel R_L)}{1 + g_m R_s} = - \frac{(R_D \parallel R_L)}{\frac{1}{g_m} + R_s}$

$A_v = - \frac{(R_D \parallel R_L)}{R_s}$