

Types of Amplifier (MOSFET based)

1. Common Source

2 Common Gate

3 Common Drain or Source Follower.

Loads in Amplifier

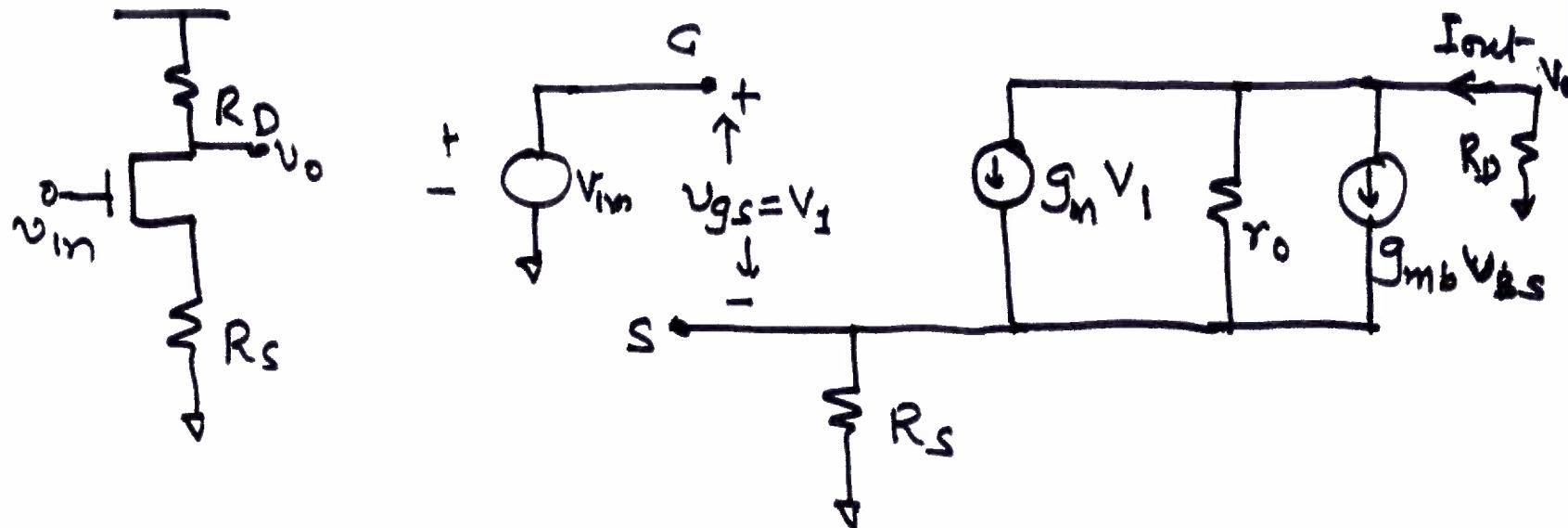
- i Diode Connected (Active Load)
- ii Current Source Load
- iii Linear Loads



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Common Source Amplifier with Source Degeneration



We know V.Gain = $g_m \cdot r_{out}$

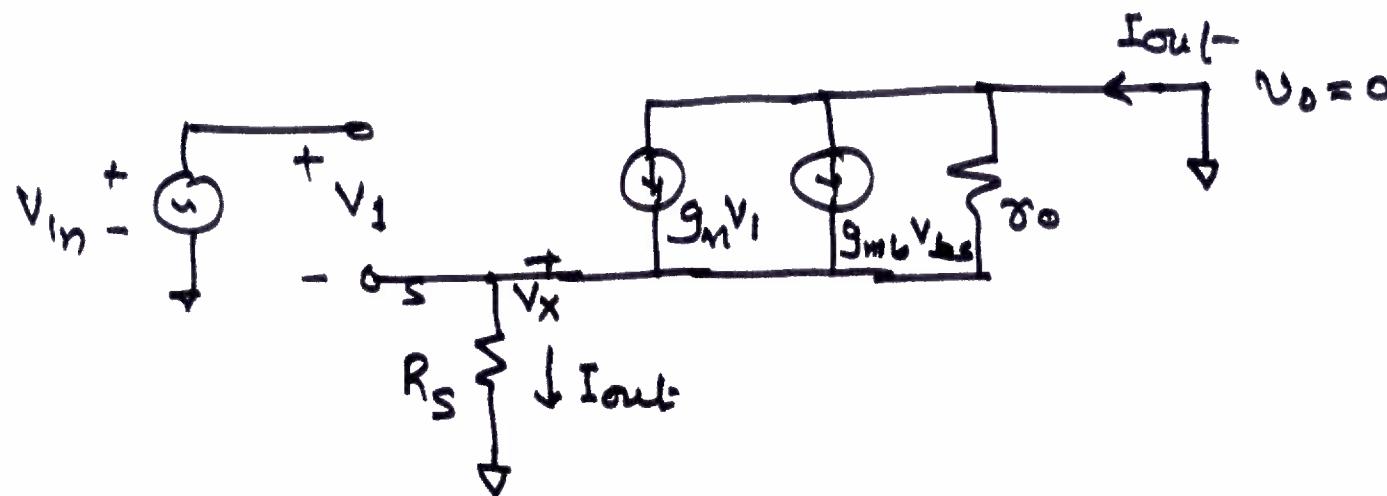
$$\text{Also } g_{m,eff} = \frac{i_{out}}{v_{in}} = \left. \frac{\partial I_{out}}{\partial V_{in}} \right|_{V_o=0}$$



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Then eq. ckt is



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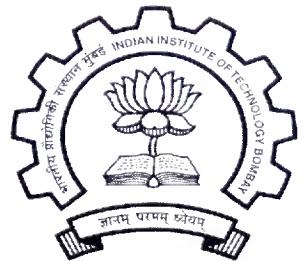
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$$\text{Now } V_{in} = V_1 + I_{out} \cdot R_s \quad \text{And} \quad V_x = I_{out} R_s$$

$$\therefore I_{out} = g_m (V_{in} - I_{out} R_s) + g_{mb} (-I_{out} R_s)$$

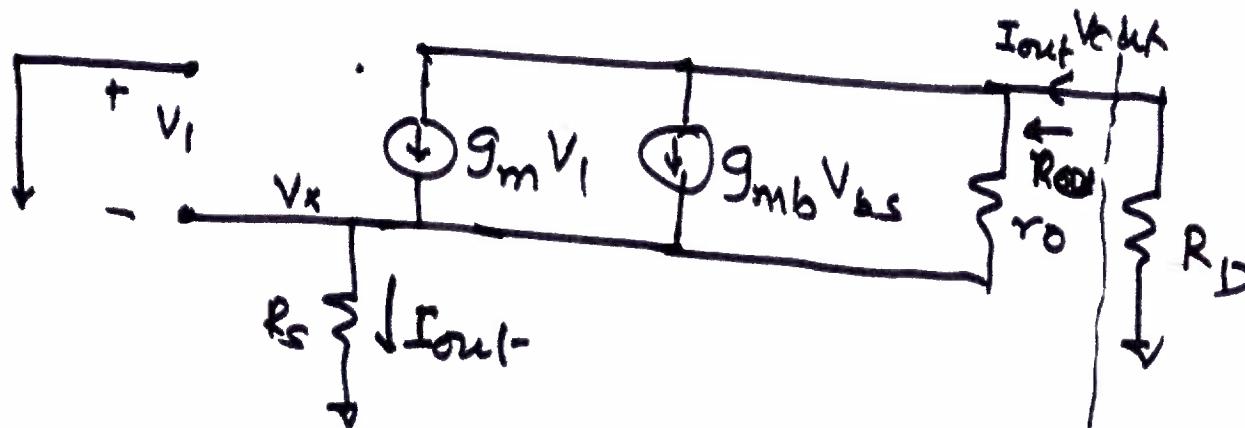
$$+ \left(\frac{0 - I_{out} R_s}{r_o} \right)$$

$$\therefore g_{m\text{eff}} = \frac{I_{out}}{V_{in}} = \frac{g_m \cdot r_o}{R_s + [1 + (g_m + g_{mb}) R_s] r_o}$$



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Now

$$V_I = -I_{out} \cdot R_s$$

$$R_o \parallel R_D = R_{out}$$

$$V_x = I_{out} \cdot R_s$$

$$\therefore I_{out} = -g_m I_{out} R_s - g_{mb} I_{out-} R_s$$

$$+ \frac{V_{out} - V_x}{R_o}$$



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$$r I_{out} \left[1 + (g_m + g_{mb}) R_s + \frac{R_s}{r_o} \right] = \frac{V_{out}}{r_o}$$

$$\therefore R_o = \frac{V_{out}}{I_{out}} = r_o \left[1 + (g_m + g_{mb}) R_s + \frac{R_s}{r_o} \right]$$

$$\therefore R_{out} = R_o \parallel R_D$$

As R_o is quite large $\approx (g_m + g_{mb}) R_s \gg r_o$

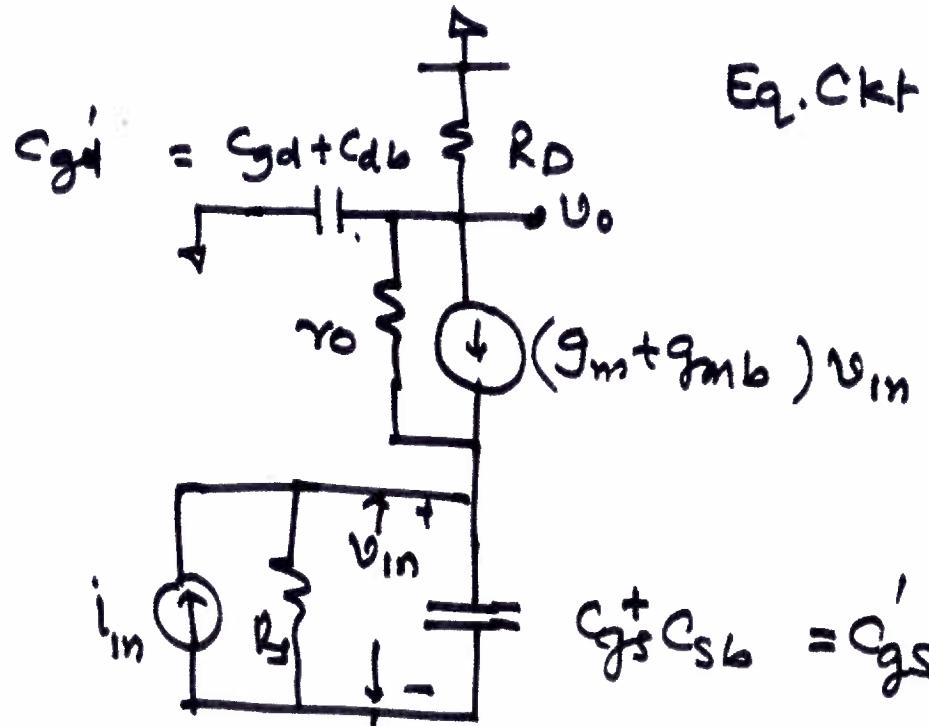
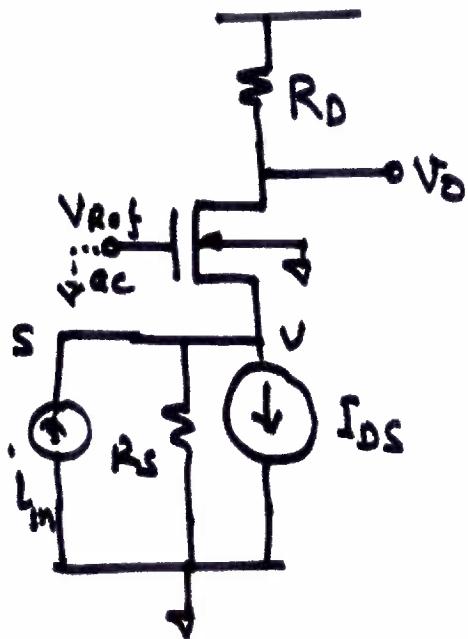
$$\therefore R_{out} \approx R_D$$

$$\therefore \text{Voltage Gain} = -g_{m\text{eff}} \cdot R_{out}$$

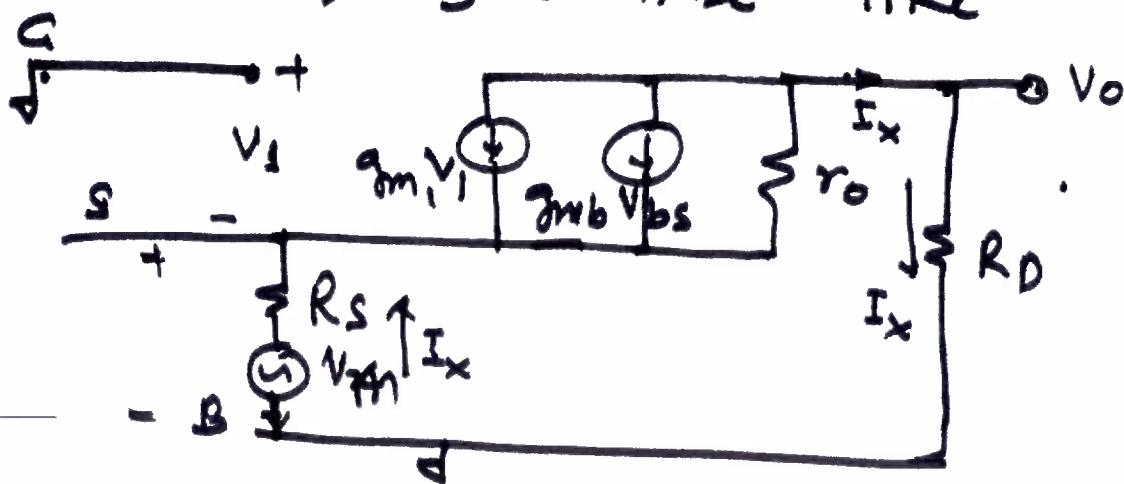
$$= -g_{m\text{eff}} R_D = \frac{-g_m r_o R_D}{R_s + [1 + (g_m + g_{mb}) R_s] r_o}$$

$$\approx -\frac{R_D}{R_s}$$

Common Gate Stage Amplifier



At Low Frequency it can be like



$$\therefore V_{sb} = * V_i$$



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We have $I_x = \frac{V_o}{R_D} \rightarrow (1)$

Further $o = V_{in} - I_x R_S + V_1 \rightarrow (2) a$

or $V_1 + V_{in} - \frac{V_o}{R_D} R_S = o \rightarrow (2) b$

The $V_o = i_{r_0} \cdot r_0 - V_1 \rightarrow (3)$

where $i_{r_0} = I_x - g_m V_1 - g_{mb} V_1 \rightarrow (4)$

$$\therefore V_o = \left(\frac{V_o}{R_D} - g_m V_1 - g_{mb} V_1 \right) r_0 - V_1$$

or $V_o = r_0 \left[-\frac{V_o}{R_D} - (g_m + g_{mb}) \left(\frac{V_o R_S}{R_D} - V_{in} \right) \right] + V_{in}$

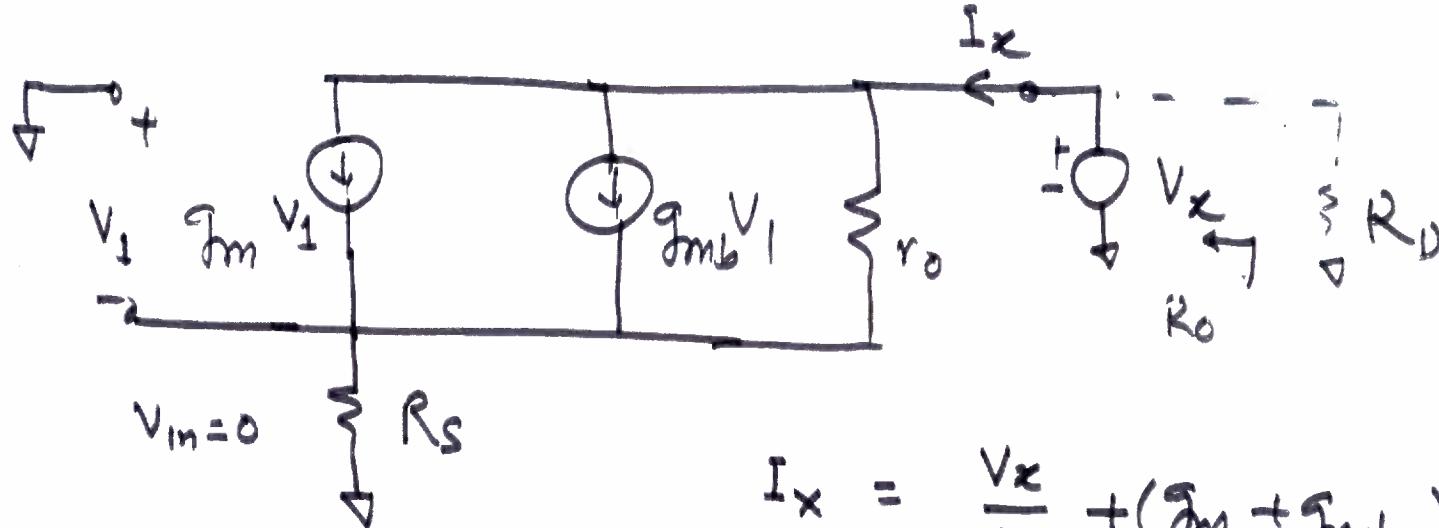
$$\therefore \frac{V_o}{V_{in}} = A_{vo} = \frac{(g_m + g_{mb}) r_0 + 1}{r_0 + (g_m + g_{mb}) r_0 R_S + R_S + R_D} \cdot R_D$$



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Output Impedance R_o



$$I_x = \frac{V_x}{r_o} + (g_m + g_{mb}) V_1$$

But $I_x R_s = -V_1$

$$\therefore I_x = \frac{V_x}{r_o} + (g_m + g_{mb}) (-I_x R_s)$$

$$I_x [1 + (g_m + g_{mb}) R_s] = \frac{V_x}{r_o}$$

$$\therefore R_o = \frac{V_x}{I_x} = r_o [1 + (g_m + g_{mb}) R_s]$$



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$$\text{Then } R_{\text{out}} = R_0 \parallel R_D$$

Since $R_0 \approx (g_m + g_{mb}) R_s r_o$ is quite large

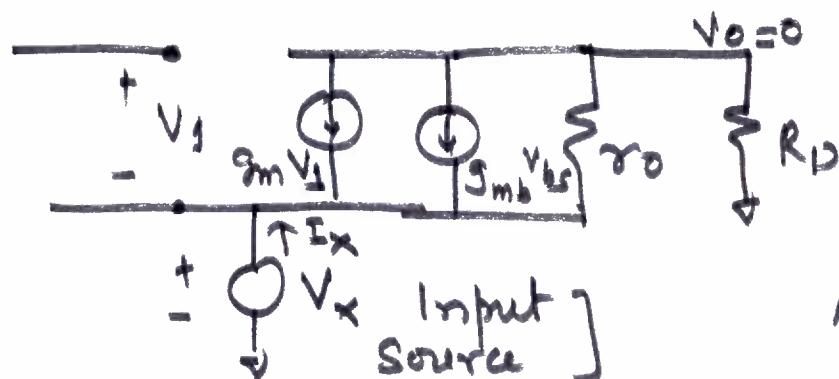
$$\therefore R_0 \gg R_D \text{ & Then } R_{\text{out}} = R_D$$

However if $R_0 < R_D$ (Current source load)

$$\text{Then } R_{\text{out}} = R_0$$

Input Resistance/Impedance

$$\text{Take } V_O = 0, V_x = -V_I$$



$$Y_{in} = \frac{I_x}{V_x} = \frac{-(g_m V_x + g_{mb} V_x) + (-\frac{V_x}{r_o})}{V_x}$$

$$= g_m + g_{mb} + \frac{1}{r_o}$$

$$= [(g_m + g_{mb}) r_o + 1] / r_o$$

As $g_m r_o \gg 1 \therefore R_{in}$ is quite low



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$$\text{Current Gain} = A_i = \frac{i_o}{i_{in}} = \frac{I_x}{I_x} = 1$$

At Low Frequency $A_i = 1$ but will be function frequency through RC effect at higher frequency.

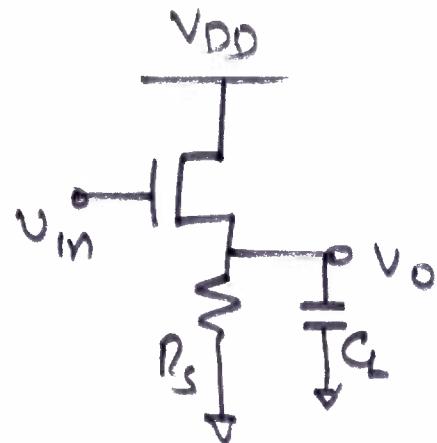
In CA Amplifier R_{in} is low but R_{out} is quite high

Or to say we can convert a normal current source into a great current source, which can be voltage controlled

Common Drain Amplifier (Source Follower)



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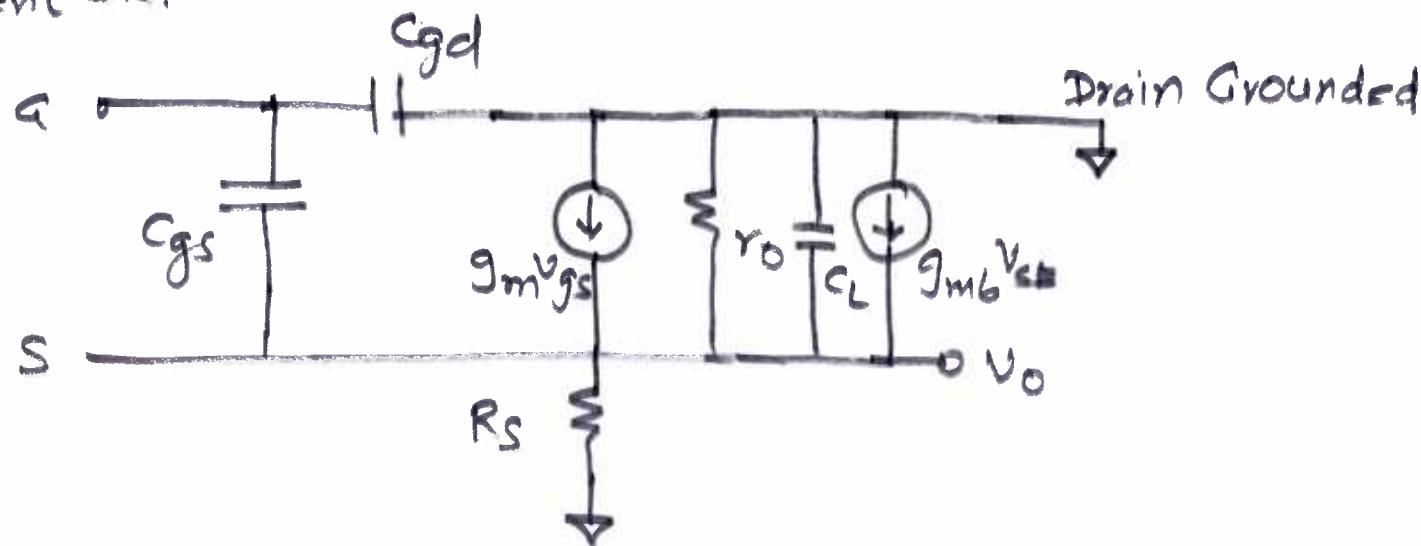
For $V_{in} > V_{TH}$

When Transistor is 'ON'

V_o follows V_{in} and hence the Circuit is called Source Follower

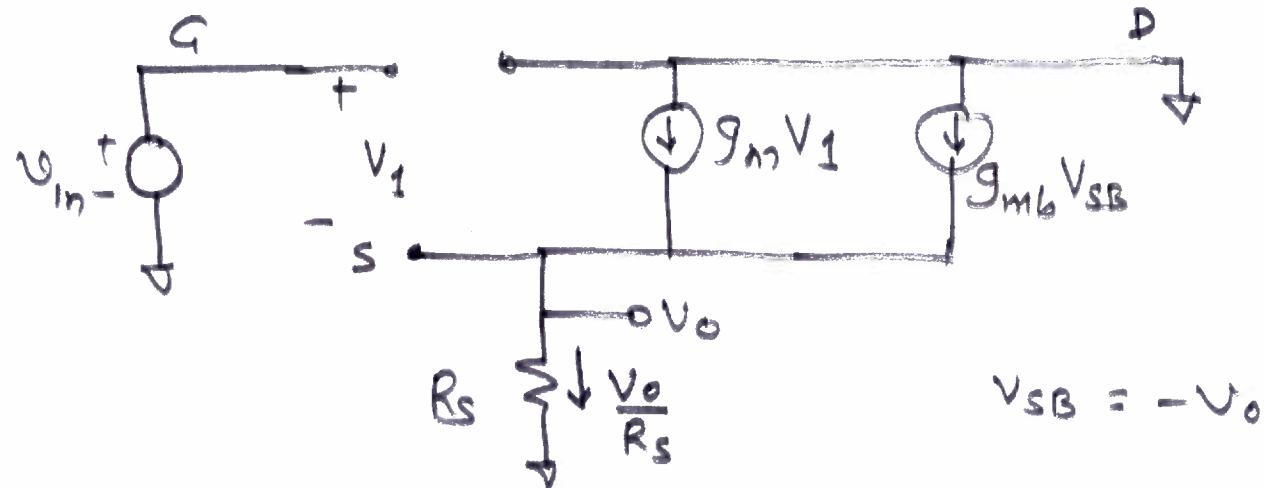
for ac circuit Drain is Grounded and hence the name Common Drain .

Equivalent Ckt



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Low Frequency Circuit is :



$$V_{SB} = -V_o$$

$$\frac{V_o}{R_s} = g_m V_1 + g_m b V_{SB} = g_m V_1 - g_m b V_o$$

$$\approx V_o \left(\frac{1}{R_s} + g_m b \right) = g_m V_1 \quad - \textcircled{1}$$

$$\text{Input Side} \quad V_{in} = V_o + V_1$$

$$\text{or} \quad V_1 = -V_o + V_{in} \quad - \textcircled{2}$$



Substituting ② in ①

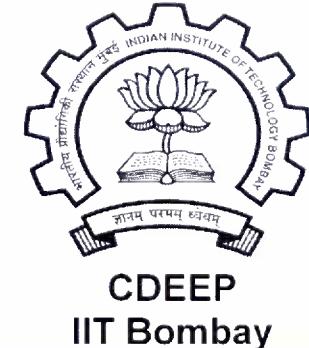
$$V_o \left(\frac{1}{R_s} + g_{mb} \right) = -g_m V_o + g_m V_{in}$$

$$\text{or } V_o \left[\frac{1}{R_s} + g_m + g_{mb} \right] = g_m V_{in}$$

$$\text{or } V_o \left[1 + (g_m + g_{mb}) R_s \right] = g_m R_s \cdot V_{in}$$

$$\text{or } A_{vo} = \frac{V_o}{V_{in}} = \frac{g_m R_s}{1 + (g_m R_s + g_{mb} R_s)}$$

(< 1)
In Phase
Output.



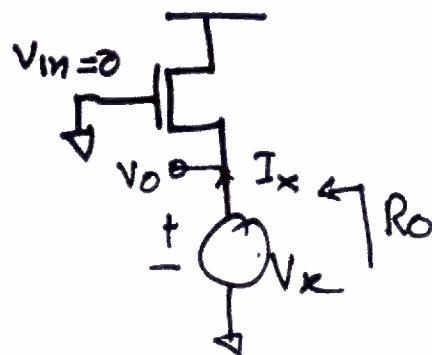
Input Impedance is only due to Capacitance at the Input side and hence very large as Input capacitance $\approx C_{gd} + C_{db} + C_{gs}$



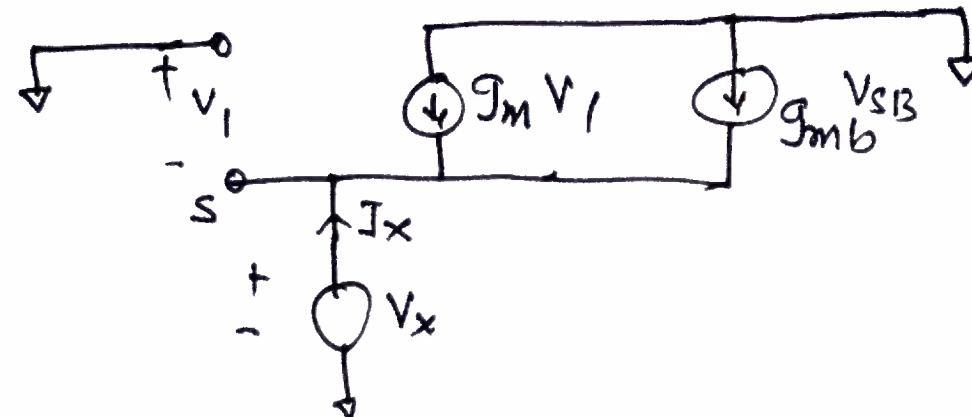
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Output Impedance



$$R_o = \frac{V_x}{I_x}$$



$$\text{As } V_i = -V_x$$

$$V_{SB} = -V_x$$

$$\therefore I_x = g_m V_x + g_{mb} V_x$$

$$\therefore R_o = \frac{V_x}{I_x} = \frac{1}{g_m + g_{mb}}$$

Where one uses Source Followers

I. Buffer : High Input Impedance
Low Output Impedance



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Problems for Designer :

(i) V_T is now function of V_O through Body Bias.

(ii) Which leads to changes in I_{DS}
& or V_{ov} with V_O

If (iii) R_s is low then effect of (ii) is even worse.

(iv) Input & Output Swings are lowered.

$$\text{If } V_{DD} = 1.2V \quad V_T = 0.4V \text{ and } V_{OV} = 0.2V$$

Then $V_h \approx 0.4 + 0.2V = 0.6V$ which is half of V_{DD} .

Thus for low Power, Low Voltage application Source Follower is not good as it reduces 'Swings'

(v) Small Swing applications such as LNA and Pre-Amplifier in Signal Processing System, one uses CD Amplifier



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Uses of ED Amplifiers (Cont.)

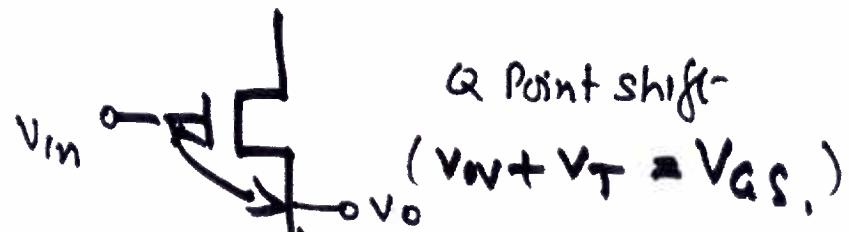
2. As Level Shifter

Output Q-Point is lower than
Input Q-Point



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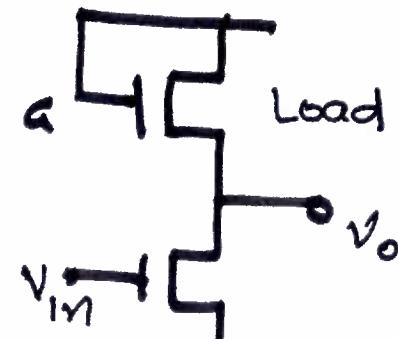
3. As Load devices in Common Source Amplifiers



Q Point shift-

$$(v_{ov} + v_T = V_{GS})$$

Q Point of output is $(v_{ov} + v_T)$
lower than Q point at Input



Saturated ~~load~~ Load acts
as current source