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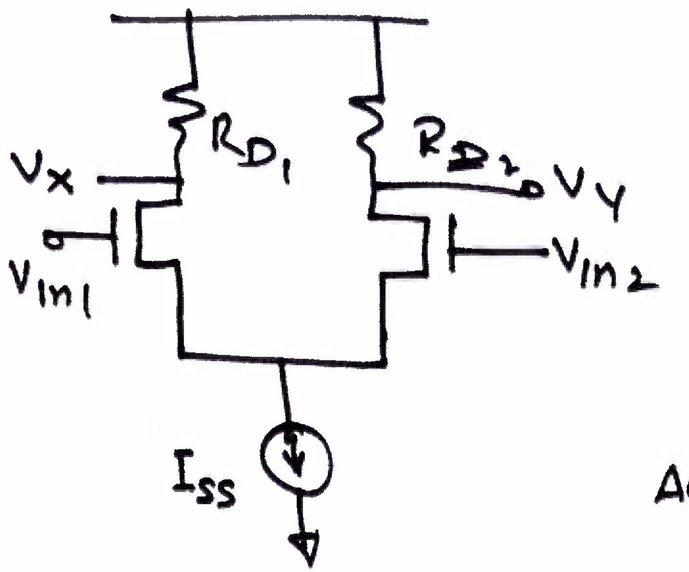
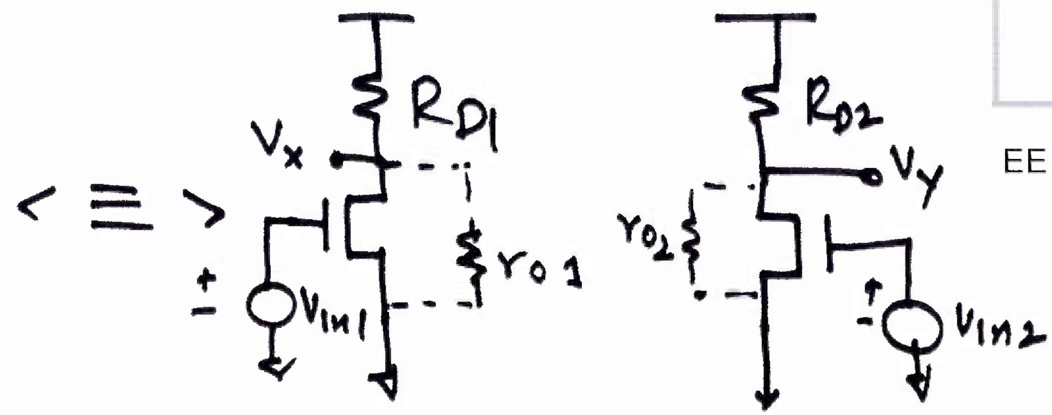
Using Half Circuit theorem we have





AC analysis will show

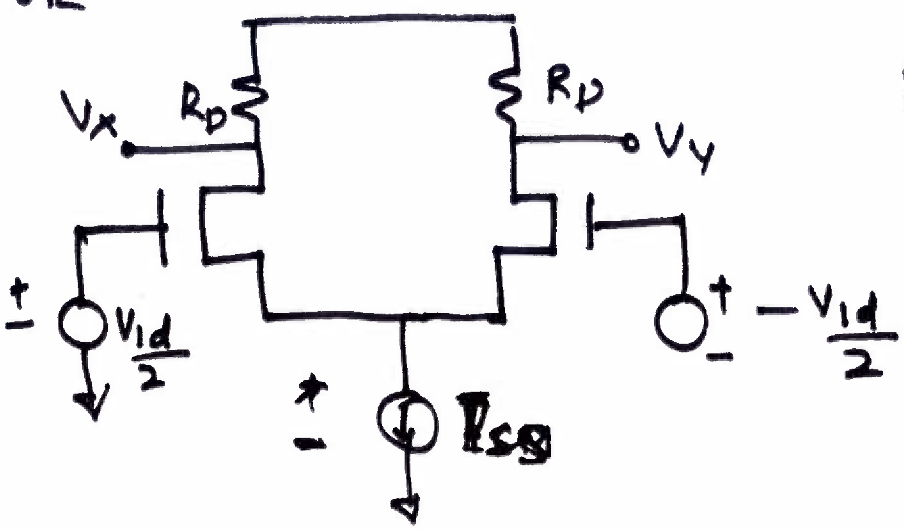
$$\frac{V_x}{\Delta V_{in1}} = -g_{m1}(R_{D1} \| r_{o1})$$

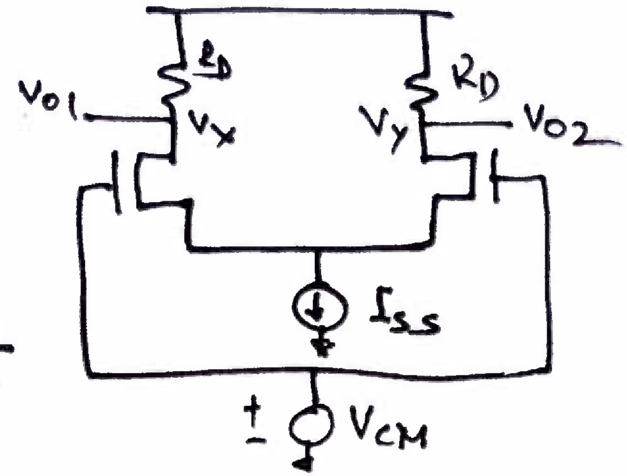
& $$\frac{V_y}{-\Delta V_{in1}} = \frac{V_y}{+\Delta V_{in2}} = -g_{m2}(R_{D2} \| r_{o2})$$

or $$\frac{V_x - V_y}{\Delta V_{in1}} = \frac{V_x - V_y}{\Delta V_{in}} = \frac{\Delta V_o}{\Delta V_{in}} = A'_{vo} = -g_{m1}(R_{D1} \| r_{o1}) - g_{m2}(R_{D2} \| r_{o2})$$

or $A'_{vo} = -2g_m(R_D \| r_o)$ if $g_m = g_{m1} = g_{m2}$ & $r_o = r_{o1} = r_{o2}$, $R_D = R_{D1} = R_{D2}$

OR





For Difference Mode case

$$V_x = -g_{m1}(R_D \| r_{o1})\frac{V_{id}}{2}$$

$$V_y = +g_{m2}(R_D \| r_{o2})\frac{V_{id}}{2}$$

or $$\frac{V_x - V_y}{V_{id}} = -g_m(R_D \| r_o)$$

$$\left\{ \begin{array}{l} g_m = g_{m1} = g_{m2} \\ r_{o1} = r_{o2} = r_o \end{array} \right.$$

or $$A_{V_{DM}} = -g_m R_D \quad \text{if } r_o \gg R_D$$

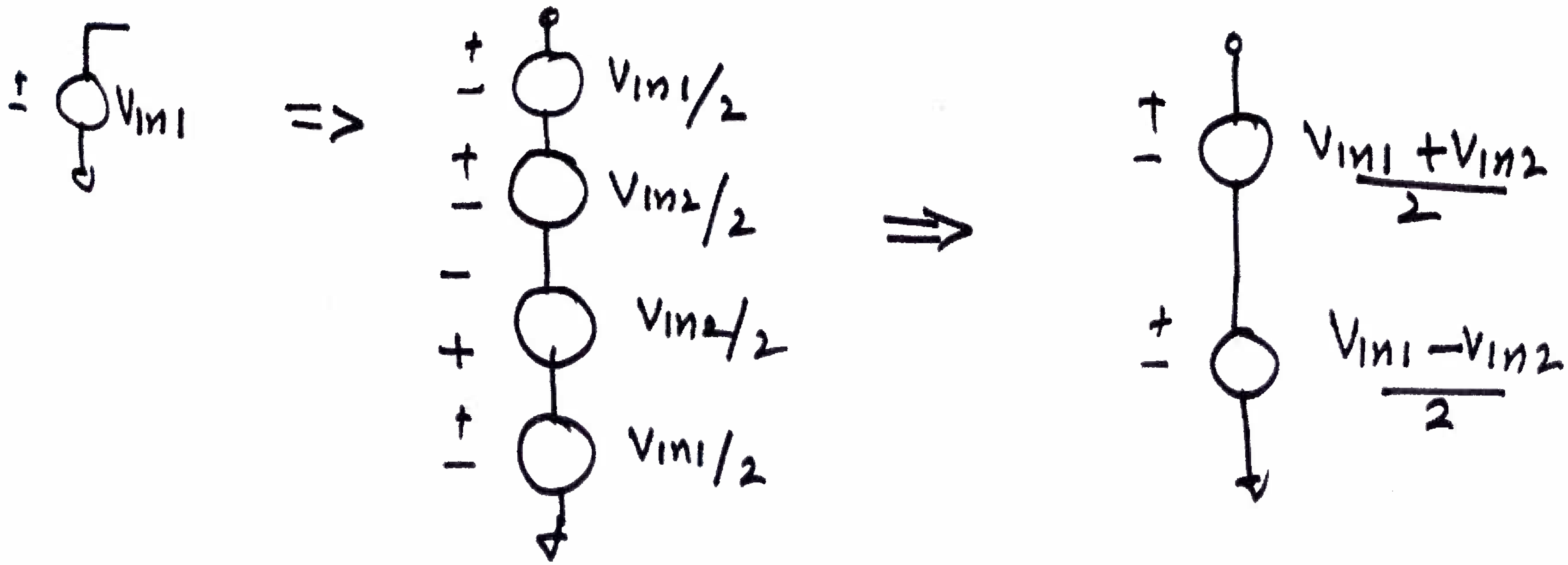
For Common Mode $V_{in1} = V_{in2} = V_{CM} = \frac{V_{in1}+V_{in2}}{2}$

$$\therefore \quad A_{V_{CM}} = \frac{V_x - V_y}{V_{CM}} = \frac{V_x - V_x}{V_{CM}} = 0$$

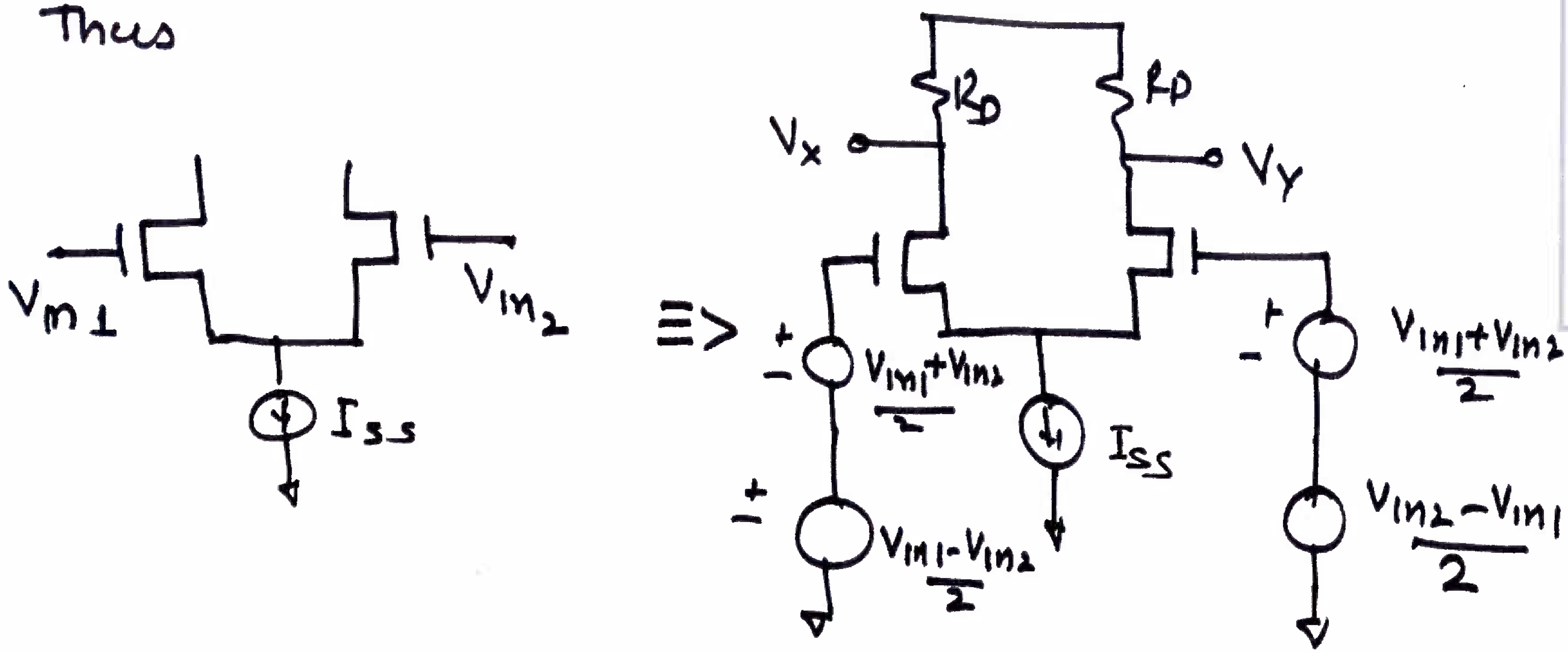
However Av_{DM} is defined as $\frac{\Delta V_o}{2\Delta V_{in}}$

or $$\frac{V_{o1} - V_{o2}}{V_{in1} - V_{in2}} = \frac{V_x - V_y}{V_{in1} - V_{in2}} = Av_{DM} = -g_m(R_D \| r_o)$$

In case two inputs are not Fully Differential, we still can use Half Ckt concept



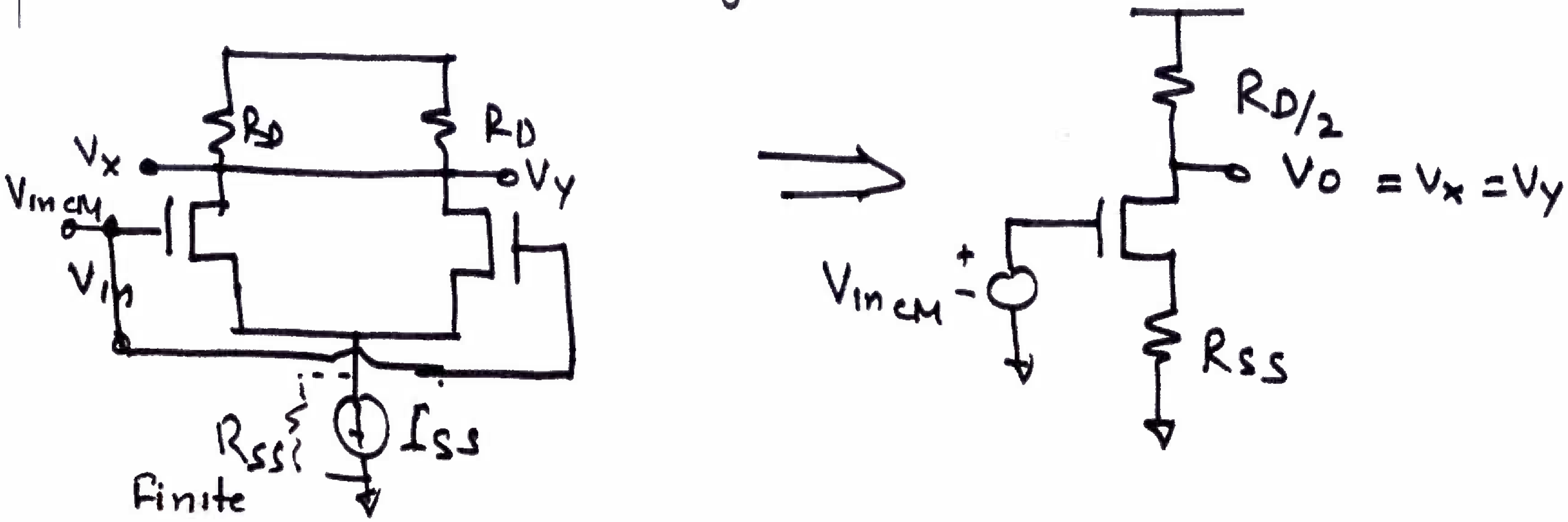
Thus

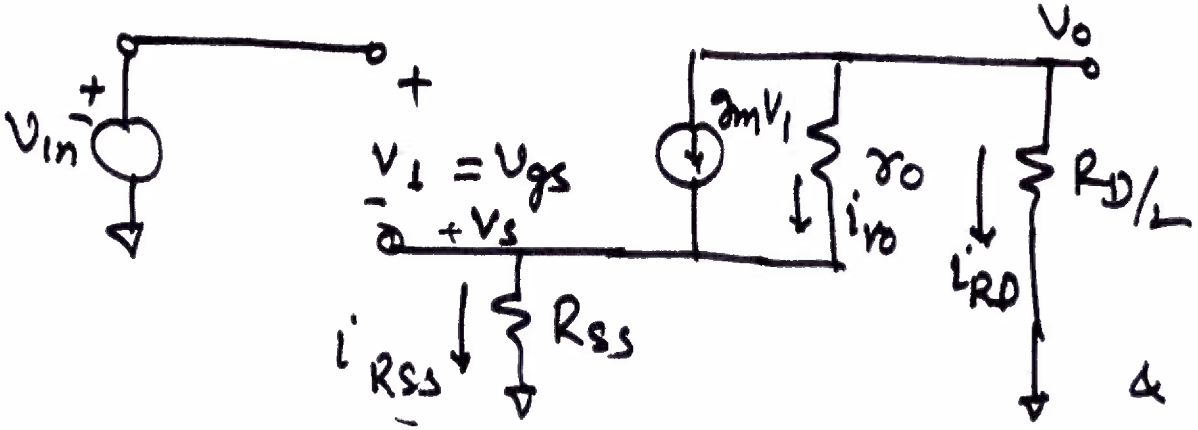


We define V_{CM} = Common Mode Voltage $= \dfrac{V_{in1}+V_{in2}}{2}$

& V_{DM} = Difference Mode Voltage $= V_{id} = V_{in1} - V_{in2}$

We relook A_{vCM} again





$$i_{RD} = -i_{Rss}$$

$$V_s = i_{Rss} \cdot R_{ss}$$

$$i_{RD} = \frac{V_o}{R_D/2}$$

$$\& \quad g_m V_1 + i_{ro} = i_{Rss} = -i_{RD}$$

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EE 618 L 11 / Slide 6

$$v_1 = v_{in} - v_s = v_{in} + \frac{v_o}{R_D/2} R_{SS}$$

$$i_{ro} = I_{R_{SS}} - g_m v_1$$

$$= -\frac{v_o}{R_D/2} - g_m \left(v_{in} + \frac{v_o R_{SS}}{R_D/2} \right)$$

$$= -\frac{2v_o}{R_D}(1 + g_m R_{SS}) - g_m v_{in}$$

$$\therefore \quad v_o = v_s + i_{ro} \cdot r_o$$

$$= -\frac{2v_o}{R_D} R_{SS} - \frac{2v_o}{R_D}(1 + g_m R_{SS}) r_o - g_m r_o v_{in}$$

$$\text{or} \quad \frac{v_o}{v_{incM}} = A_{vcM} = \frac{-2 g_m r_o (R_D/2)}{R_D + 2R_{SS} + 2r_o(1 + g_m R_{SS})} \qquad \text{If } R_{SS} \gg \frac{1}{g_m}$$

$$= \frac{-R_D/2}{1/g_m + R_{SS}} \cong -\frac{R_D}{2R_{SS}} = \frac{-g_m R_D}{2(1 + g_m R_{SS})}$$

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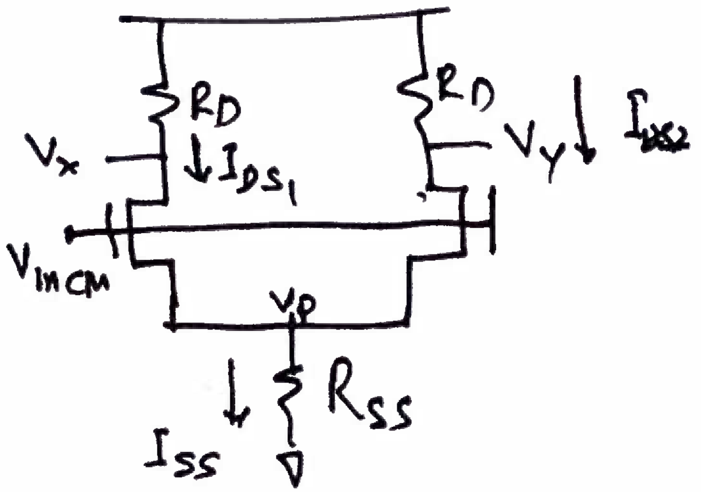
If M_1 & M_2 do not have Identical Characteristics, due to Mismatches, then $V_x \neq V_y$

Then from the eq. circuit of symmetry cannot be used.



$\therefore I_{DS1} = g_{m1}(V_{inCM} - V_p)$

& $I_{DS2} = g_{m2}(V_{inCM} - V_p)$

or $V_p = (I_{DS1} + I_{DS2}) \cdot R_{SS}$

$$= \frac{(g_{m1} + g_{m2})R_{SS}}{1 + (g_{m1} + g_{m2})R_{SS}} V_{inCM}$$

Then $V_x = -I_{DS1} \cdot R_D$

$$V_x = -g_{m1}(V_{inCM} - V_p)R_D = \frac{-g_{m1}R_D}{1 + (g_{m1} + g_{m2})R_{SS}} V_{inCM}$$

Similarly $V_y = -\dfrac{g_{m2}R_D V_{inCM}}{1 + (g_{m1} + g_{m2})R_{SS}}$

$$\therefore\ V_x - V_y = -\frac{(g_{m1} - g_{m2})R_D}{1 + (g_{m1} + g_{m2})R_{SS}} V_{INCM}$$

or

$$A_{VCM-DM} = -\frac{\Delta g_m R_D}{1 + g_m R_{SS}}$$

Where $\Delta g_m = g_{m1} - g_{m2}$

$g_m = g_{m1} + g_{m2}$

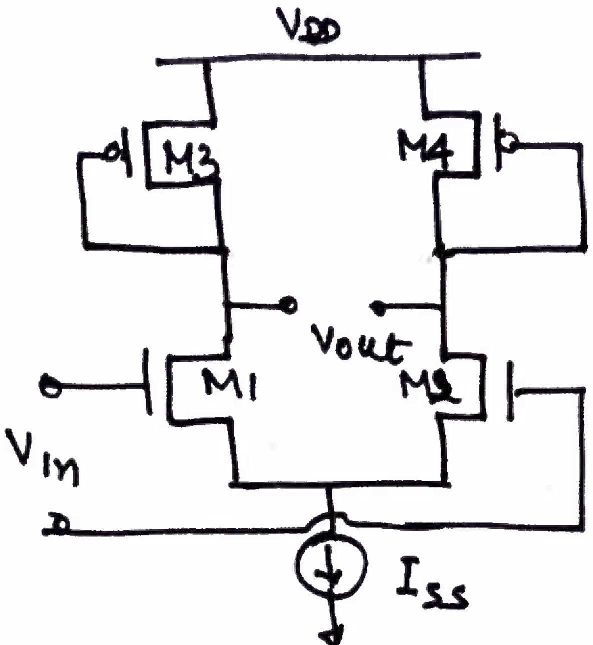
Then we define $CMRR = \frac{A_{VDM}}{A_{VCM-DM}}$

$$\cong \frac{2 g_m}{\Delta g_m}(1 + g_m R_{SS})$$

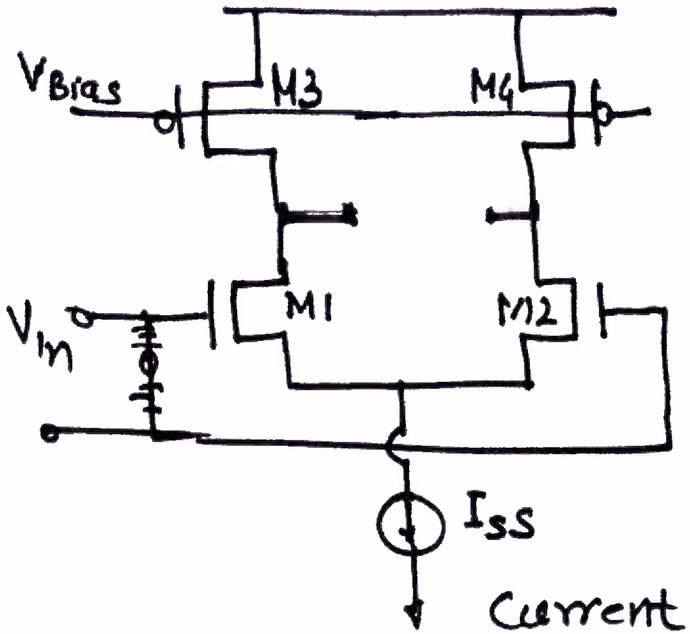
$\therefore$ For larger CMRR requirements

1. g_m's be larger $\leftarrow$ I_{DS}, W/L
2. R_{SS} be large
3. Mismatch be minimised: $\Delta g_m \rightarrow$ smaller

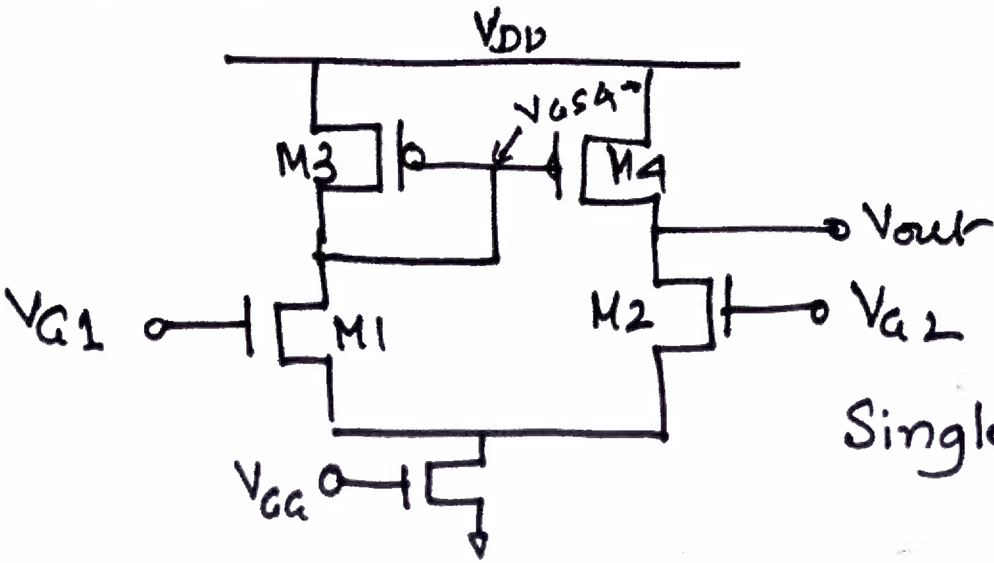
DIFFAMP WITH DIFFERENT LOADS



Diode Connected Load



Current Source Load



Single Ended DIFFAMP

For Diode connected load :—

$$R_{Load} = g_{mp}^{-1} \parallel r_{op} \simeq g_{mp}^{-1}$$

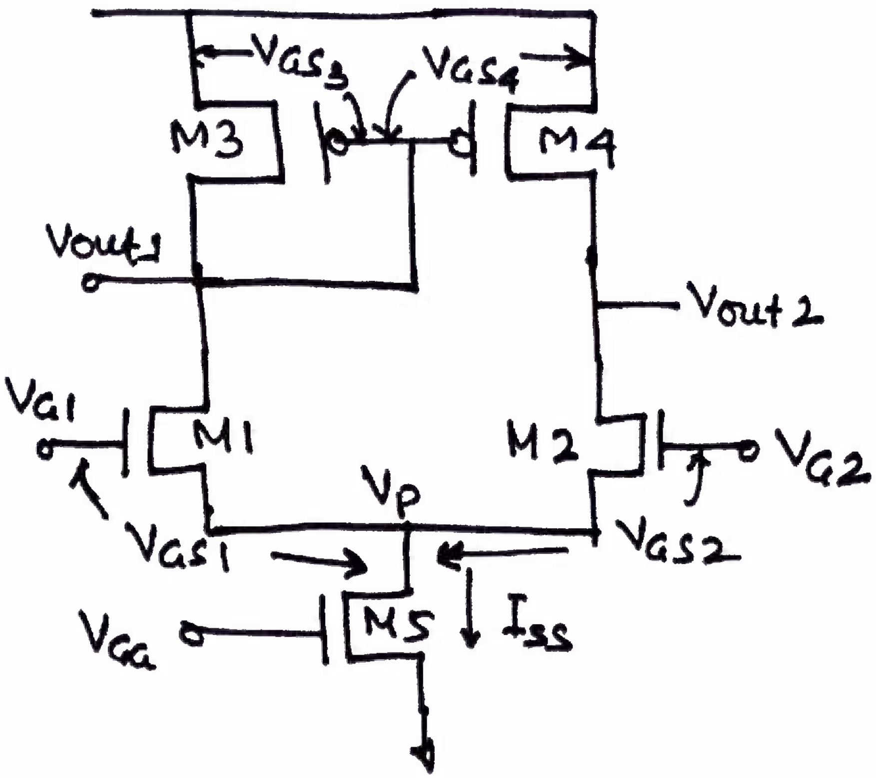
Then $$A_V = -\frac{g_{mn}}{g_{mp}} = \sqrt{\frac{\mu_n}{\mu_p} \cdot \frac{(W/L)_n}{(W/L)_p}}$$

For Current Source load :

$$R_{Load} = r_{op}$$

$$\therefore \quad A_V = -g_{mn} (r_{on} \parallel r_{op})$$

Current Sink (Current Mirror) DIFFAMP



$$V_{G1} - V_{G2} = V_{id}$$

or $V_{G1} = +\frac{V_{id}}{2}$

$V_{G2} = -\frac{V_{id}}{2}$

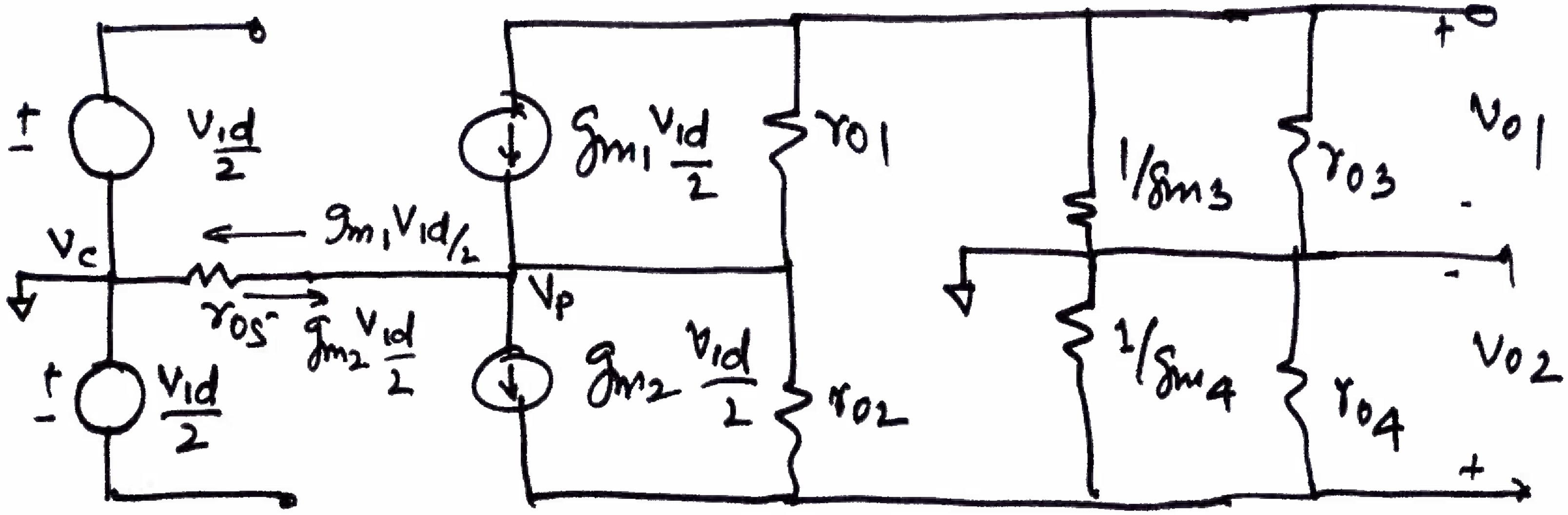
$V_{GS_1} = V_{G1} - V_P =$

$V_{GS2} = V_{G2} - V_P =$

As M5 is acting as current source I_{SS} which gives r_{o5} = Output resistance of M_5 based current source and $\rightarrow \infty$ (v. high)

Hence we can assume: $V_P \rightarrow 0$

We can see through the eq. circuit and can arrive that $V_p \to 0$.

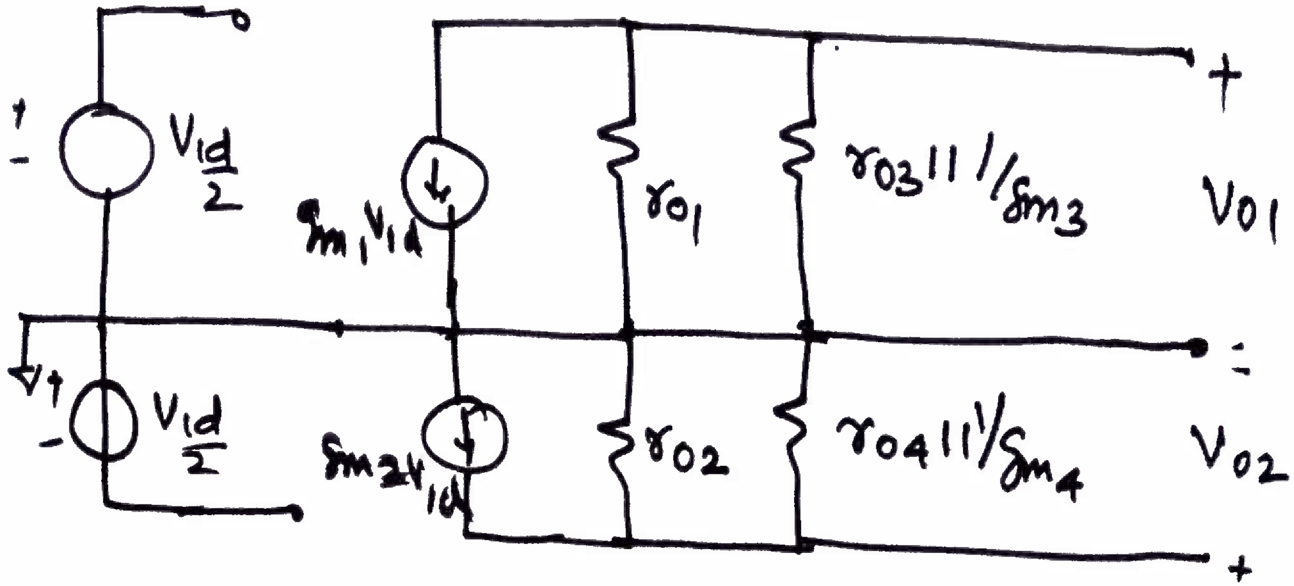


(i) Here we assume that $\frac{V_{o1}}{r_{o1}}$ and $\frac{V_{o2}}{r_{o2}}$ is very small compared to $g_{m1}\frac{V_{id}}{2}$ & $g_{m2}\frac{V_{id}}{2}$

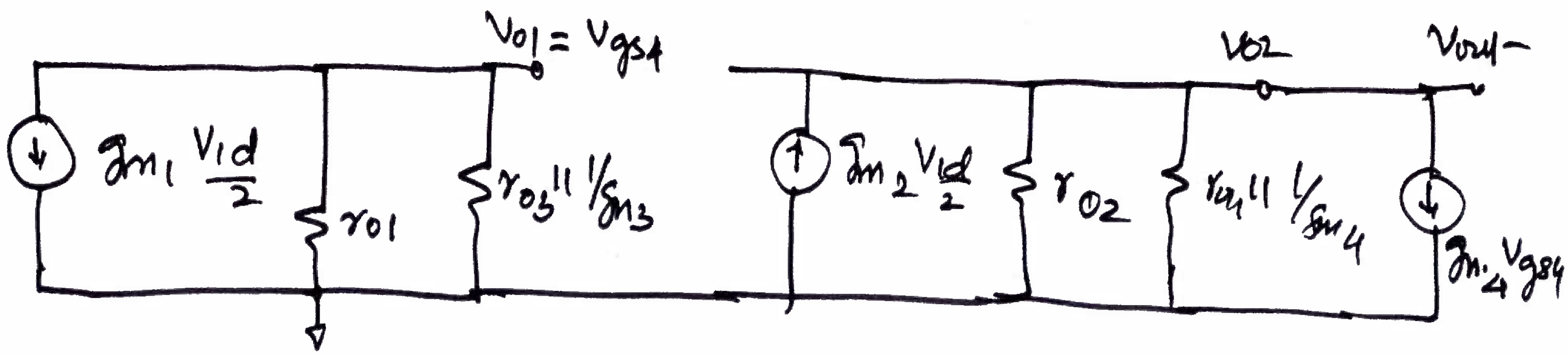
(ii) And if g_{m1} and g_{m2} do not differ then

Current through $r_{o5} = g_{m1}\frac{V_{id}}{2} - g_{m2}\frac{V_{id}}{2} \simeq 0$

Hence $V_p = V_c = 0$



Vid/2
gm1 Vid
ro1
ro3 || 1/gm3
Vo1
+
-
Vid/2
gm2 Vid
ro2
ro4 || 1/gm4
Vo2
+



Vo1 = Vgs4
gm1 Vid/2
ro1
ro3 || 1/gm3
gm2 Vid/2
ro2
ro4 || 1/gm4
Vo2
gm4 Vgs4