## Module - 4

## Unit - 4

## Feedback in Amplifiers

## Review Questions:

1. What are the drawbacks in a electronic circuit not using proper feedback?
2. What is positive feedback? Positive feedback is avoided in amplifier circuits. Discuss.
3. Explain negative feedback. In what ways does it modify amplifier performance?
4. What are the different types of negative feedbacks? Draw appropriate circuits.
5. Write the basic expression for voltage gain of an amplifier with feedback and explain the terms used in the expression.
6. What type of feedback will you use in an amplifier to
(i) Increase input impedance
(ii) Decrease input impedance
7. Draw the gain-frequency response of an RC coupled amplifier. Discuss fall in gain at very low and at very high frequencies.
8. Two identical amplifier stages having gains of 50 each and phase shift between input and output signals of $180^{\circ}$ each are cascaded. How much is the over all gain and phase shift of 2-stage amplifier?
9. What are the different methods of coupling signals used in amplifiers? Discuss their advantages and disadvantages.
10. Two transistors having current gains of 100 and 80 respectively form Darlington pair. How much is current gain of the Darlington pair?

## Problems:-

4.1 In the series - shunt feedback amplifier shown in fig. calculate the voltage gains without feedback, $A$, and with feedback $A_{\text {FB }}$.


## Solution:-

The open loop gain $A$, is defined as the ratio of output voltage, $v_{0}$. to the error voltage, $\mathrm{v}_{\mathrm{e}}$, which is input to the basic amplifier.

Therefore,
$\mathrm{A}=\frac{\mathrm{v}_{\mathrm{o}}}{\mathrm{v}_{\mathrm{e}}}=\frac{1 V}{5 \times 10^{-6} V}=2 \times 10^{5}$
$A=2 \times 10^{5}$
Voltage gain of feedback amplifier, $\mathrm{A}_{\mathrm{FB}}$, is expressed as,

$$
A_{F B}=\frac{A}{1+A B}
$$

Where $B$ is gain of the feedback network.
Since $A B \gg 1$

$$
A_{F B}=\frac{A}{A B}=\frac{1}{B}
$$

And $B$ is,

$$
B=\frac{R_{1}}{R_{1}+R_{2}}=\frac{1 k}{1 k+99 k}=\frac{1}{100}=0.01
$$

Therefore,

$$
\begin{aligned}
& A_{F B}=\frac{1}{B}=\frac{1}{0.01}=100 \\
& A_{F B}=100
\end{aligned}
$$

4.2 Common emitter (CE) amplifier shown in fig. has voltage gain of 200 when $R_{E}=0$. Stability is brought through negative feedback by adding resistor $\mathrm{R}_{\mathrm{E}}$. Calculate the value of resistor $R_{E}$ using feedback concepts so that final voltage gain ( $=A_{F B}$ ) is equal to 100 .


## Solution:-

The value of gain of feedback network, B can be obtained using the basic feedback relation,

$$
A_{F B}=\frac{A}{1+A B}
$$

Since $A_{F B}=100$ and $A=200$, we have
$100=\frac{200}{1+200 \times B} \approx \frac{1}{B}$
or $B=\frac{1}{100}$
Further, the value of $B$ is given by the ratio,
$B=\frac{R_{E}}{R_{C}}$
or, $R_{E}=B R_{C}=\frac{6 \times 10^{3}}{100}=60 \Omega$
$R_{E}=60 \Omega$
4.3 The feedback amplifier shown in fig. makes use of an op amp with internal gain (open loop gain) $\mathrm{A}=10^{5}$. How much is the output voltage for input signal $\mathrm{v}_{\mathrm{s}}=2 \mathrm{mV}$ in the circuit shown.


## Solution:-

The gain of feedback amplifier,
$A_{\text {FB, }}$, is

$$
A_{F B}=\frac{A}{1+A B} \cong \frac{1}{B}
$$

## Because $\mathrm{AB} \gg 1$

And, gain of feedback network, B, is

$$
B=\frac{R_{1}}{R_{1}+R_{2}}=\frac{1 k}{1 k+49 k}=\frac{1}{50}
$$

Therefore,

$$
A_{F B}=\frac{1}{B}=50
$$

And the output voltage, $v_{0}$, is
$\mathrm{v}_{\mathrm{o}}=\mathrm{A}_{\mathrm{FB}} \mathrm{X} \mathrm{v}_{\mathrm{s}}$

$$
=50 \times 2 \mathrm{mV}
$$

or $\mathrm{v}_{0}=100 \mathrm{mV}$
4.4 An amplifier exhibits distortion in the form of voltage fluctuations of $\sim 10 \%$. These fluctuations are to be restricted to $\sim 1 \%$ by incorporating negative feedback in the amplifier circuit. If ultimate gain is desired to be 120, what should be the open loop gain of the amplifier?

## Solution:-

The distribution, $D_{F B}$ in an amplifier with feedback and distortion, $D$ without feedback in the amplifier are related as,
$D_{F B}=\frac{D}{(1+A B)}$
Where $A$ is open loop gain of amplifier and $B$ is the gain of feedback network.
Now, $D=10 \%=\frac{10}{100}$ (given)
and $D_{F B}=1 \%=\frac{1}{100}$ (given)
Therefore, using above equation,
$\frac{1}{10}=\frac{1}{(1+A B)}$
or, $(1+A B)=10$
Now, $A_{F B}=\frac{A}{(1+A B)}=\frac{A}{10}$
or, $A=10 \times A_{F B}=10 \times 120 \quad\left(\because A_{F B}=120\right)$
or, $A=1200$
4.5 Figure shows an op amp circuit with voltage series feedback through resistors $\mathrm{R}_{1}$ and $R_{2}$. The internal gain of op amp is $5 \times 10^{4}$ and input impedance is $100 \mathrm{k} \Omega$. Find out the gain and input impedance of the amplifier with feedback.


## Solution:-

The gain of amplifier with feedback, $A_{F B}$, is
$A_{F B}=\frac{A}{1+A B}$
Where $A$ is open loop gain of amplifier and $B$ is gain of feedback network.
Since $A B \gg 1$
$A_{F B}=\frac{A}{1+A B} \approx \frac{1}{B}$
Now, $B=\frac{R_{1}}{R_{1}+R_{2}}=\frac{2 k}{2 k+198 k}=\frac{1}{100}$
Therefore,

$$
\begin{aligned}
& A_{F B}=\frac{1}{B}=100 \\
& A_{F B}=100
\end{aligned}
$$

The input impedance with and without feedback in a voltage-series circuit are related as,
$Z_{i(F B)}=(1+A B) Z_{i} \approx A B Z_{i}$
Or $Z_{i(F B)}=5 \times 10^{4} \times 10^{-2} \times 100 \mathrm{k}$
$\operatorname{Or} Z_{i(F B)}=50 \mathrm{M} \Omega$
4.6 An op amp has open loop band width of 5 Hz at gain $2 \times 10^{5}$. Feedback is incorporated and resistors $R_{1}$ and $R_{2}$ form the feedback network. Find the bandwidth of the amplifier shown in fig.


## Solution:-

The gain of feedback network, B, in the circuit of fig. is
$B=\frac{R_{1}}{R_{1}+R_{2}}=\frac{1 k}{1 k+100 k} \approx \frac{1}{100}=10^{-2}$
The bandwidth with feedback, $f_{(F B)}$ and without feedback (Open loop bandwidth), $f_{2}$, are related as
$f_{(B W)}=(1+A B) f_{2}$
Where $A$ is open loop gain of op amp
or $f_{(B W)} \approx \mathrm{ABf}_{2}$
or $f_{(B W)}=2 \times 10^{5} \times 10^{-2} \times 5 \mathrm{H}_{\mathrm{Z}}$
or $f_{(B W)}=10 \mathrm{kH}_{\mathrm{z}}$
4.7 Two RC coupled amplifiers are connected to form a 2-stage amplier. If the lower and upper cutoff frequencies of each individual amplifier respectively are $100 \mathrm{H}_{z}$ and 20 $\mathrm{kH}_{\mathrm{z}}$, What these frequencies are for the 2-stage amplifier?

## Solution:-

For multi-stage amplifier consisting of n identical amplifying stages, the lower cut off $\mathrm{f}_{1 \text { (multi) }}$ is expressed as,

$$
\begin{equation*}
f_{1(\text { multi })}=\frac{f_{1}}{\sqrt{2^{1 / n}-1}} \tag{1}
\end{equation*}
$$

Where $f_{1}$ is lower cut-off of individual stages.
And the upper cutoff $f_{2(\text { multi) }}$ is expressed as,
$f_{2(\text { multi) }}=f_{2} \sqrt{2^{1 / n}-1}$
Where $f_{2}$ is upper cut-off of individual stages.
Therefore, the lower cut-off of 2-stages amplifier (Eq(1)) is
$f_{1(2 \text {-stage })}=\frac{100}{\sqrt{2^{1 / 2}-1}}=\frac{100}{0.643}$
or $f_{1(2-\text { stage })}=155.5 H_{Z}$
Similarly, the upper cutoff of the 2-stage amplifier (Eq 2) , is

$$
\begin{aligned}
f_{2(2-\text { stage })}= & f_{2} \sqrt{2^{1 / 2}-1} \\
& 20 \times 0.643 k H_{Z} \\
\text { or } f_{2(2-\text { stage })} & =12.8 \mathrm{kH}_{Z}
\end{aligned}
$$

Thus the band width gets reduced in a multi-stage amplifier.
4.8 The two-stage amplifier shown in fig uses transistors $Q_{1}$ and $Q_{2}$, both having current gain $\beta$ of 80 and dynamic emitter resistance, $r^{\prime}$, of $25 \Omega$ each. Find out the over all voltage gain of the amplifier.


## Solution:-

In a multi-stage amplifier, it is convenient to analyse the last stage (output stage) first.
In the present case, the last stage $\left(Q_{2}-\right.$ stage $)$ is emitter follower which has voltage gain of unity. Then the over all gain of two-stage amplifier(product of gains of individual stages) is same as gain of first stage which is common emitter amplifier in fig.

The gain of CE amplifier is

$$
A_{V}=A_{V 1}=\frac{r_{c 1}}{r_{E 1}+r_{e}^{\prime}}
$$

Since $R_{E}=0$ (for $Q_{1}$ transistor), $r_{E 1}=0$
Then,

$$
A_{V}=\frac{r_{c 1}}{r_{e}^{\prime}}
$$

Where $r_{c 1}$ is the effective impedence seen by the collector of transistor $Q_{1}$.
That is,
$r_{c 1}=R_{C 1} \| Z_{i(\text { base) } 2}$
The impedance, $Z_{i(\text { base }) 2}$ at the base of transistor $Q_{2}$ is,
$Z_{i(b a s e) 2}=\beta . R_{E 2}$

$$
=80 \times 3 k
$$

or $Z_{i(\text { base }) 2}=240 \mathrm{k} \Omega$
Therefore,

$$
\begin{aligned}
\mathrm{R}_{\mathrm{c} 1} & =\mathrm{R}_{\mathrm{C} 1} \| \mathrm{Z}_{\mathrm{i}(\text { base }) 2} \\
& =10 \mathrm{k} \| 240 \mathrm{k} \\
\text { or } \mathrm{r}_{\mathrm{c} 1} & =9.6 \mathrm{k} \Omega
\end{aligned}
$$

Therefore, over all gain of amplifier of fig is

$$
A_{V}=\frac{r_{c 1}}{r_{e}^{\prime}}=\frac{9.6 \times 10^{3}}{25}=384
$$

$$
\text { or } A_{V}=384
$$

