



NPTEL ONLINE CERTIFICATION

(Funded by the MoE, Govt. of India)



This certificate is awarded to
YEDDU VENKATESH
for successfully completing the course



Hardware Modeling using Verilog

with a consolidated score of **50** %

Online Assignments	19.83/25	Proctored Exam	30/75
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Total number of candidates certified in this course: **1149**

Jul-Sep 2025

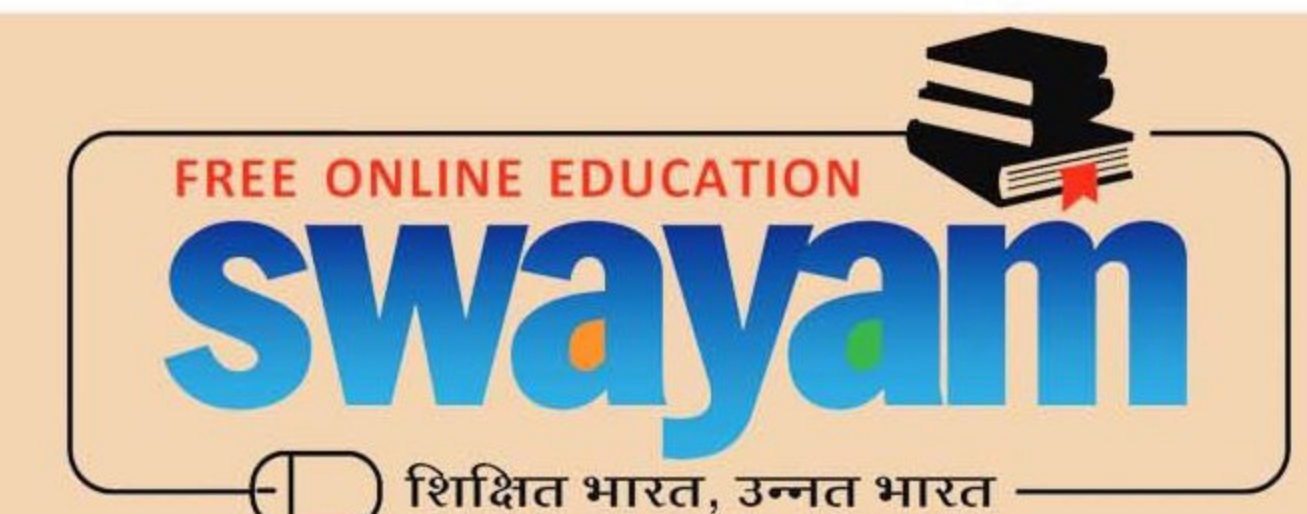
(8 week course)

Prof. Haimanti Banerji

Coordinator, NPTEL
IIT Kharagpur



Indian Institute of Technology Kharagpur



Roll No: NPTEL25CS155S536000461

To verify the certificate



No. of credits recommended: 2 or 3