



Elite

NPTEL ONLINE CERTIFICATION

(Funded by the MoE, Govt. of India)

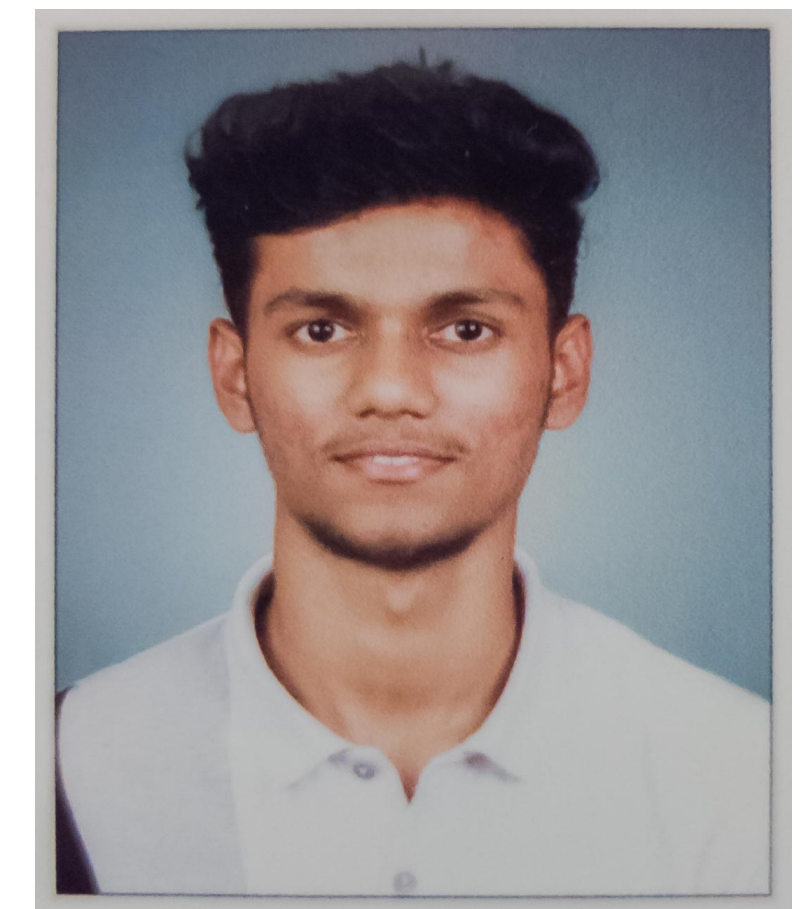


This certificate is awarded to

JERIN JOY

for successfully completing the course

VLSI Design Flow: RTL to GDS



with a consolidated score of **82** %

Online Assignments	21.88/25	Proctored Exam	60/75
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Total number of candidates certified in this course: **2041**

Dr. Anand Srivastava
Coordinator
Continued Education Program, IIITD

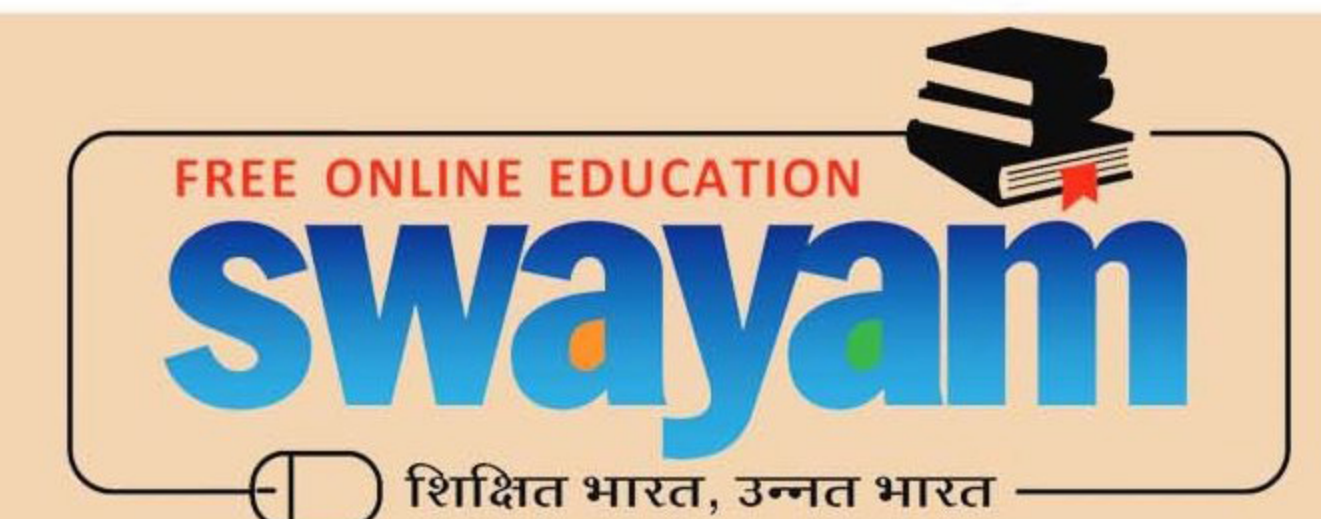
Jul-Oct 2024

(12 week course)

Prof. Andrew Thangaraj
NPTEL, Coordinator
IIT Madras



INDRAPRASTHA INSTITUTE of
INFORMATION TECHNOLOGY **DELHI**



Roll No: NPTEL24EE102S459900463

To verify the certificate



No. of credits recommended: 3 or 4