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Design and Analysis of VLSI Subsystems

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Total number of candidates certified in this course: **287**

Dr. Chandrashekar Ramanathan
Chairman
Professor and Dean (Academics), IIIT- Bangalore

Jan-Apr 2024

(12 week course)

Prof. Andrew Thangaraj
NPTEL, Coordinator
IIT Madras

INTERNATIONAL INSTITUTE OF INFORMATION TECHNOLOGY BANGALORE

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