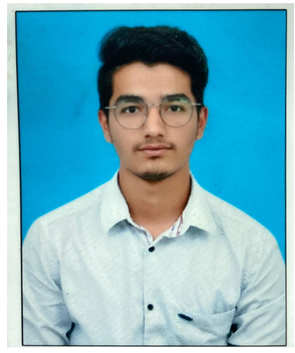
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for successfully completing the course

VLSI Design Flow: RTL to GDS

with a consolidated score of **64** %

Online Assignments	24.69/25	Proctored Exam	39.75/75

Total number of candidates certified in this course: **1877**

Dr. Anand Srivastava
Coordinator
Continued Education Program, IIITD

Jul-Oct 2023

(12 week course)

Prof. Andrew Thangaraj
NPTEL, Coordinator
IIT Madras

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